

FIG. 1

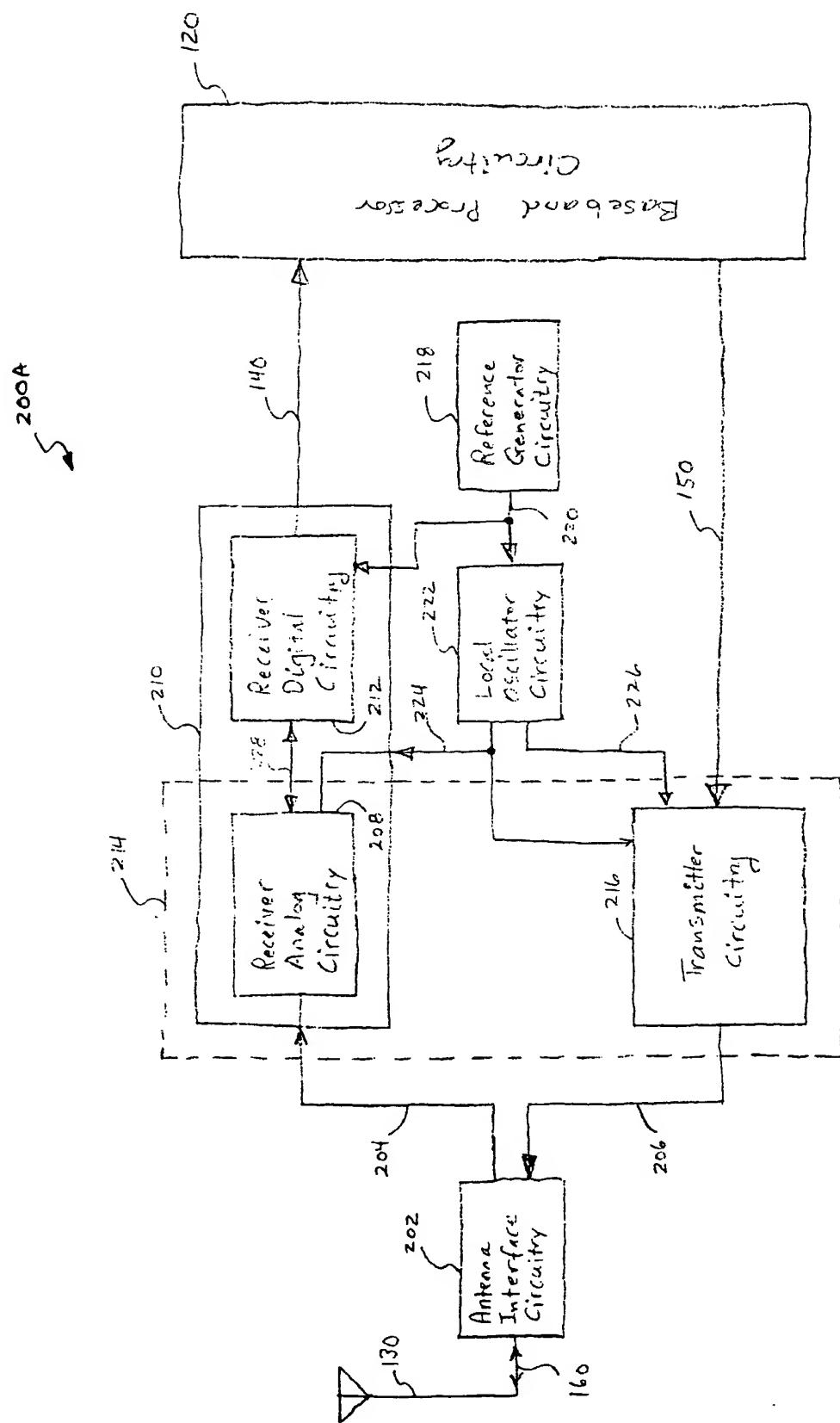


FIG. 2A

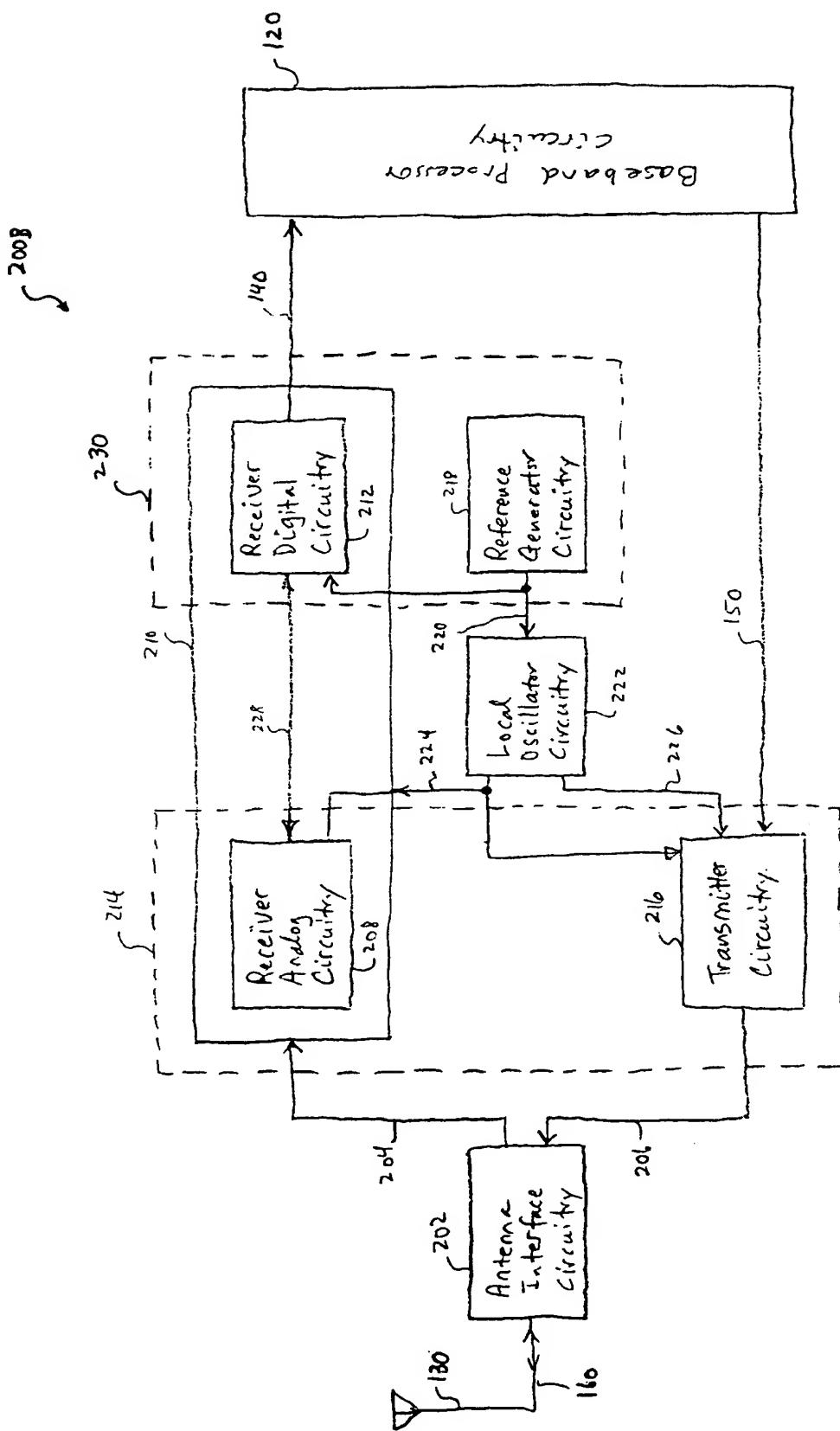


FIG. 2B

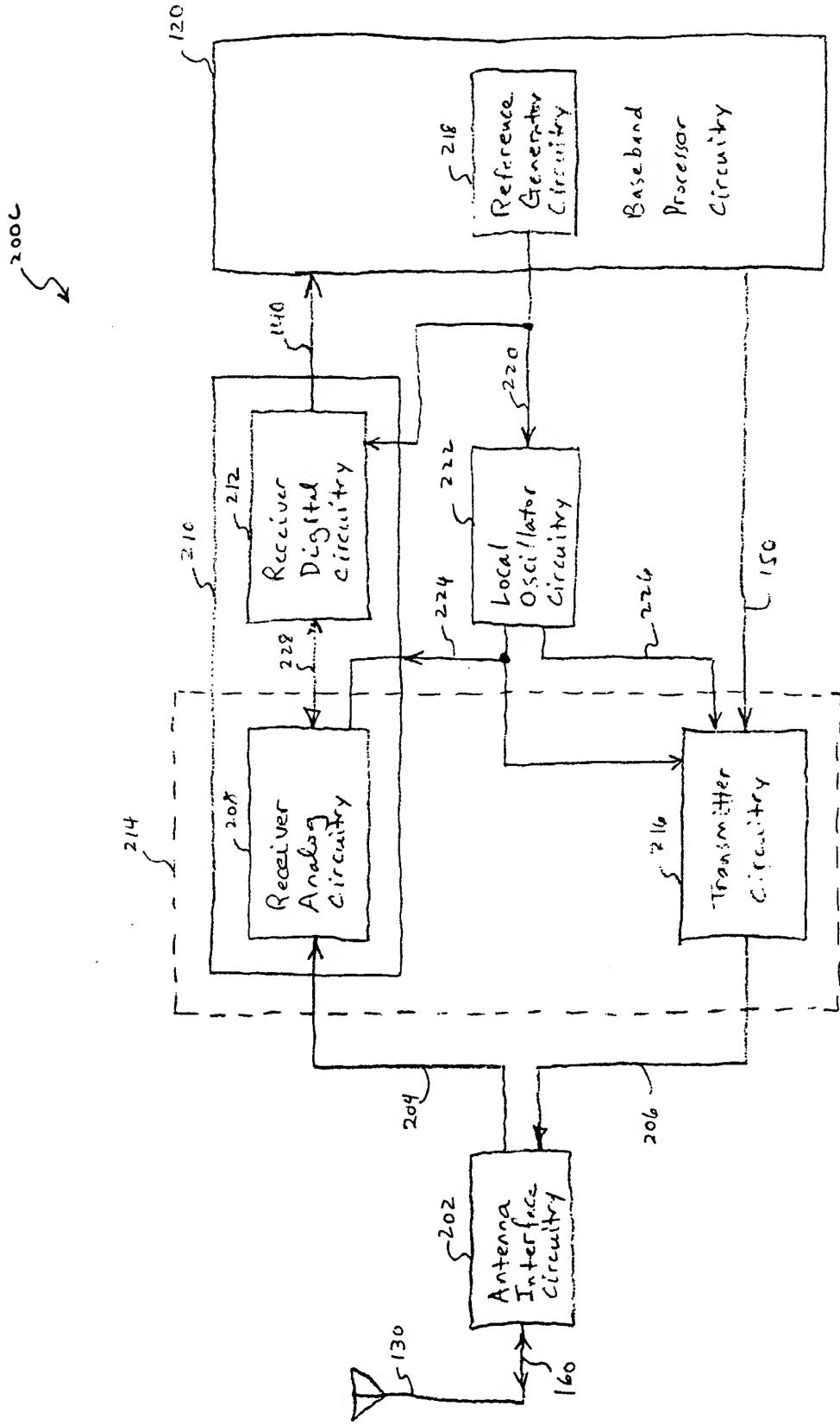


FIG. 2C

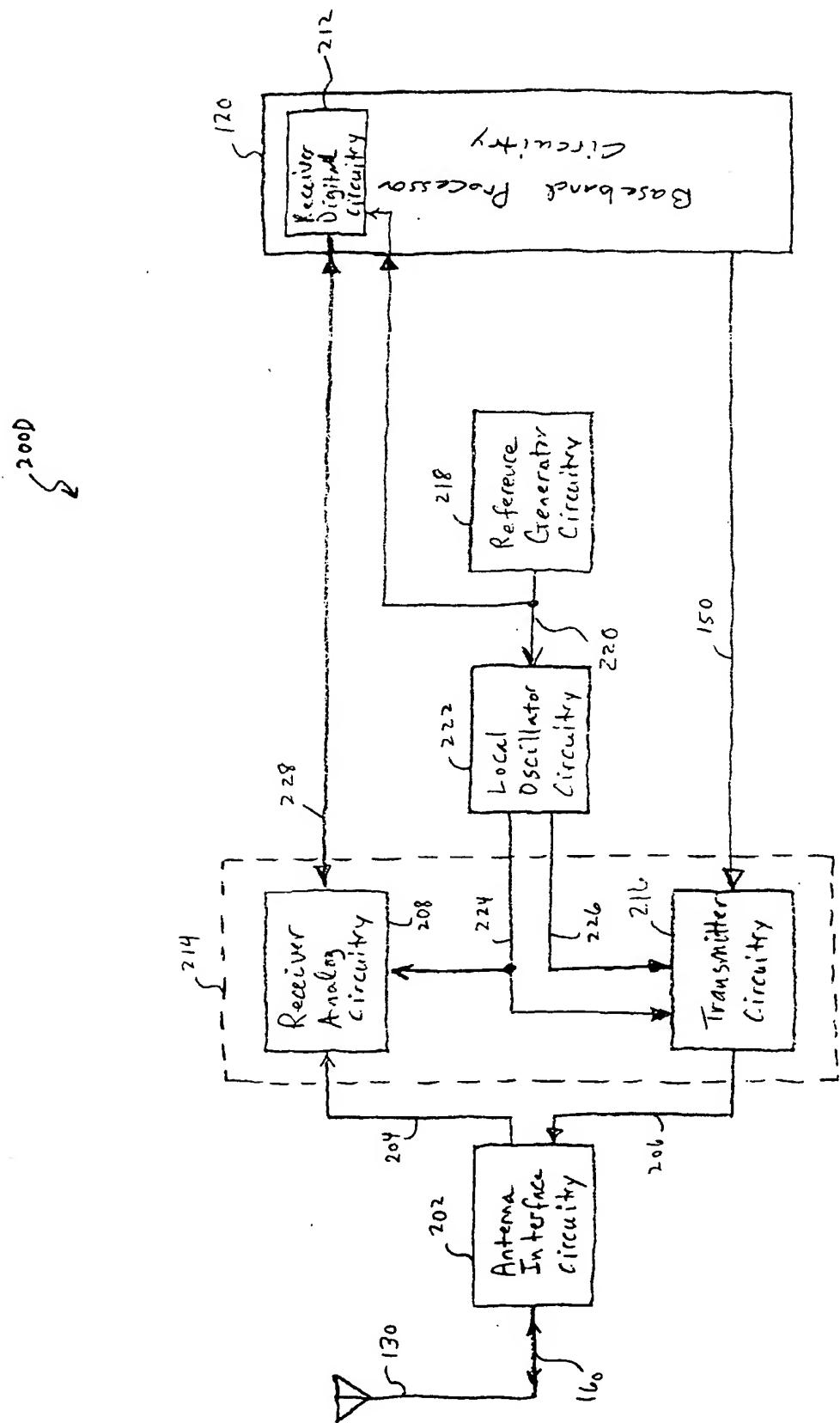


FIG. 2D

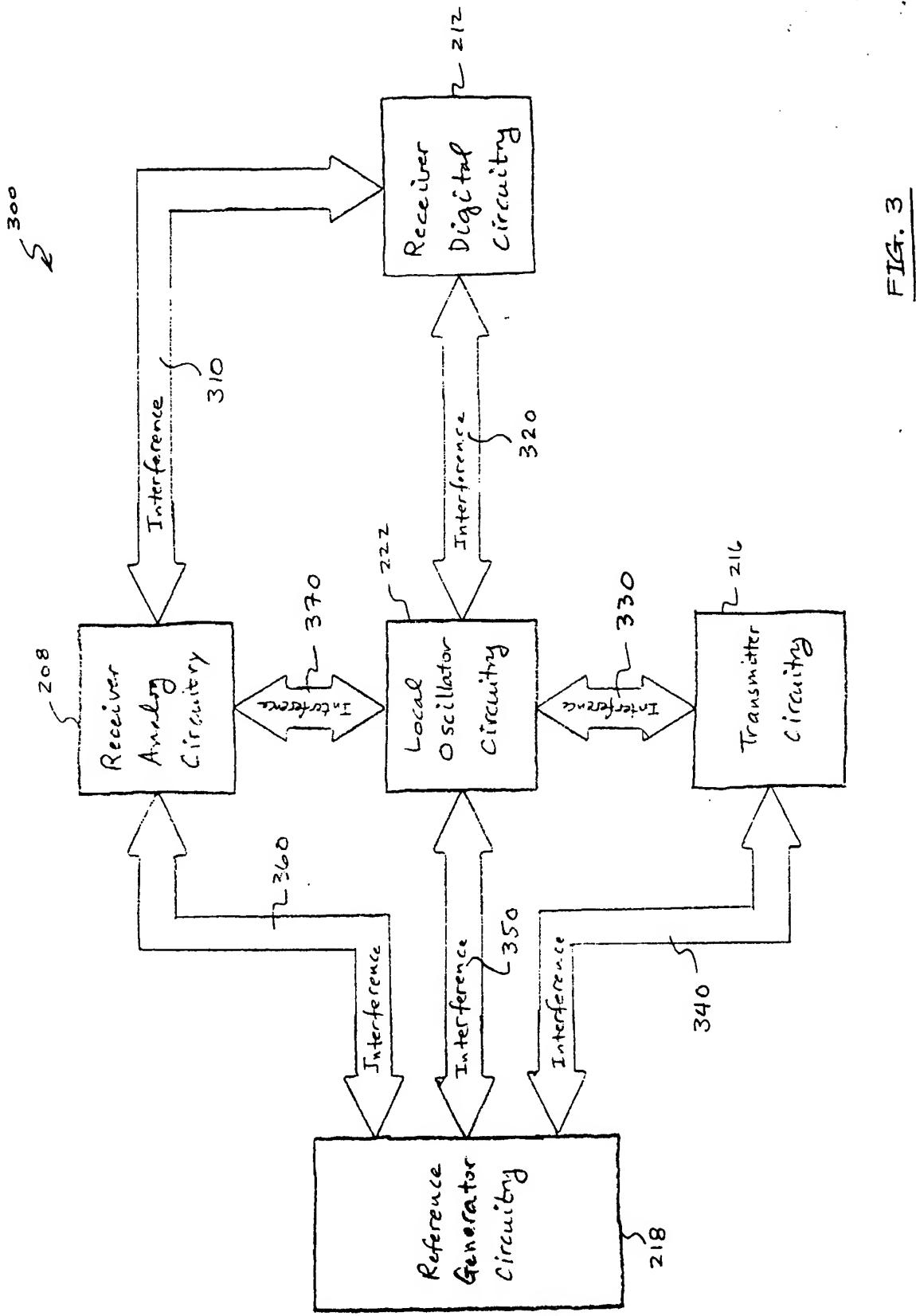


FIG. 3

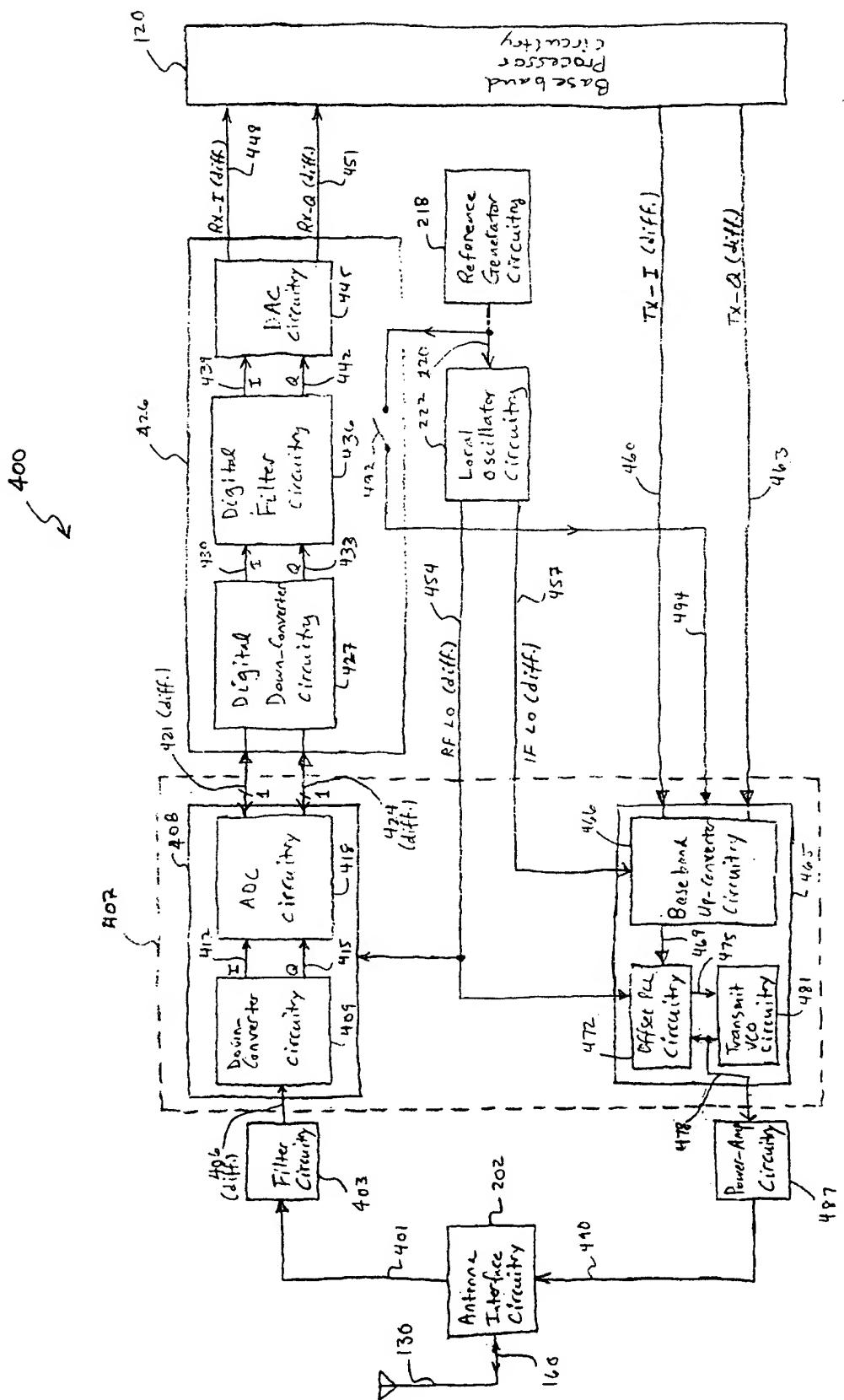


FIG. 4

500

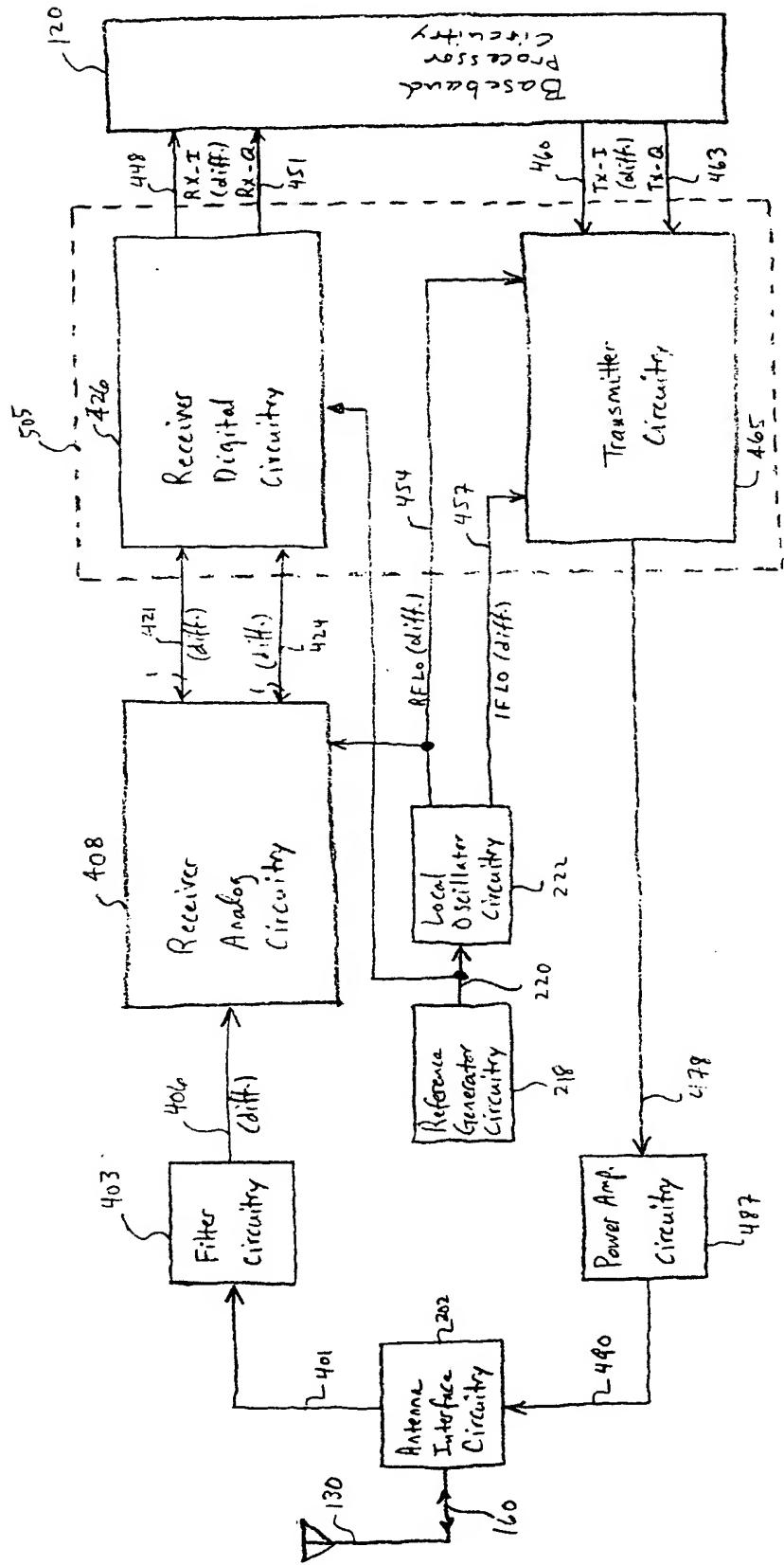


FIG. 5

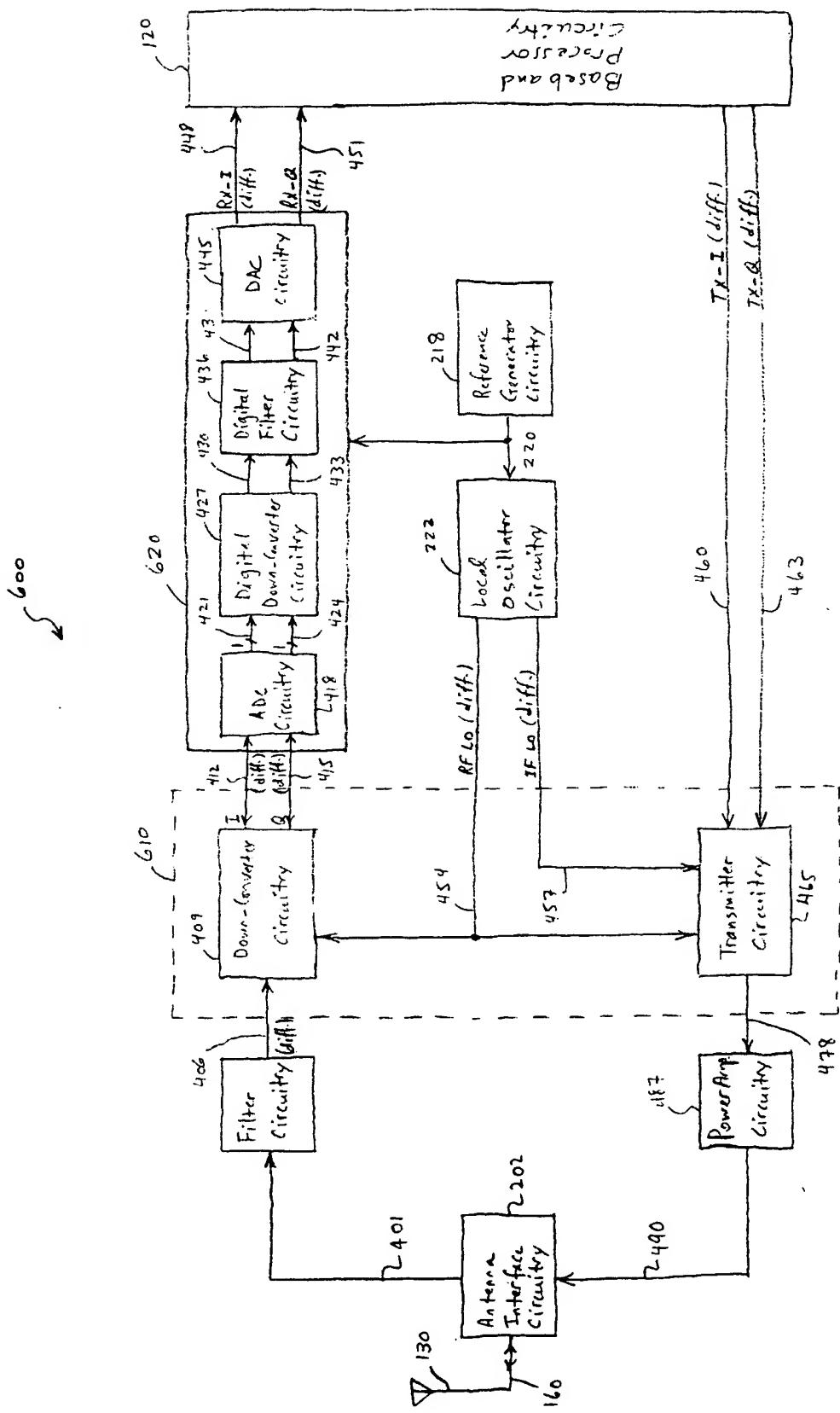


FIG. 6

700

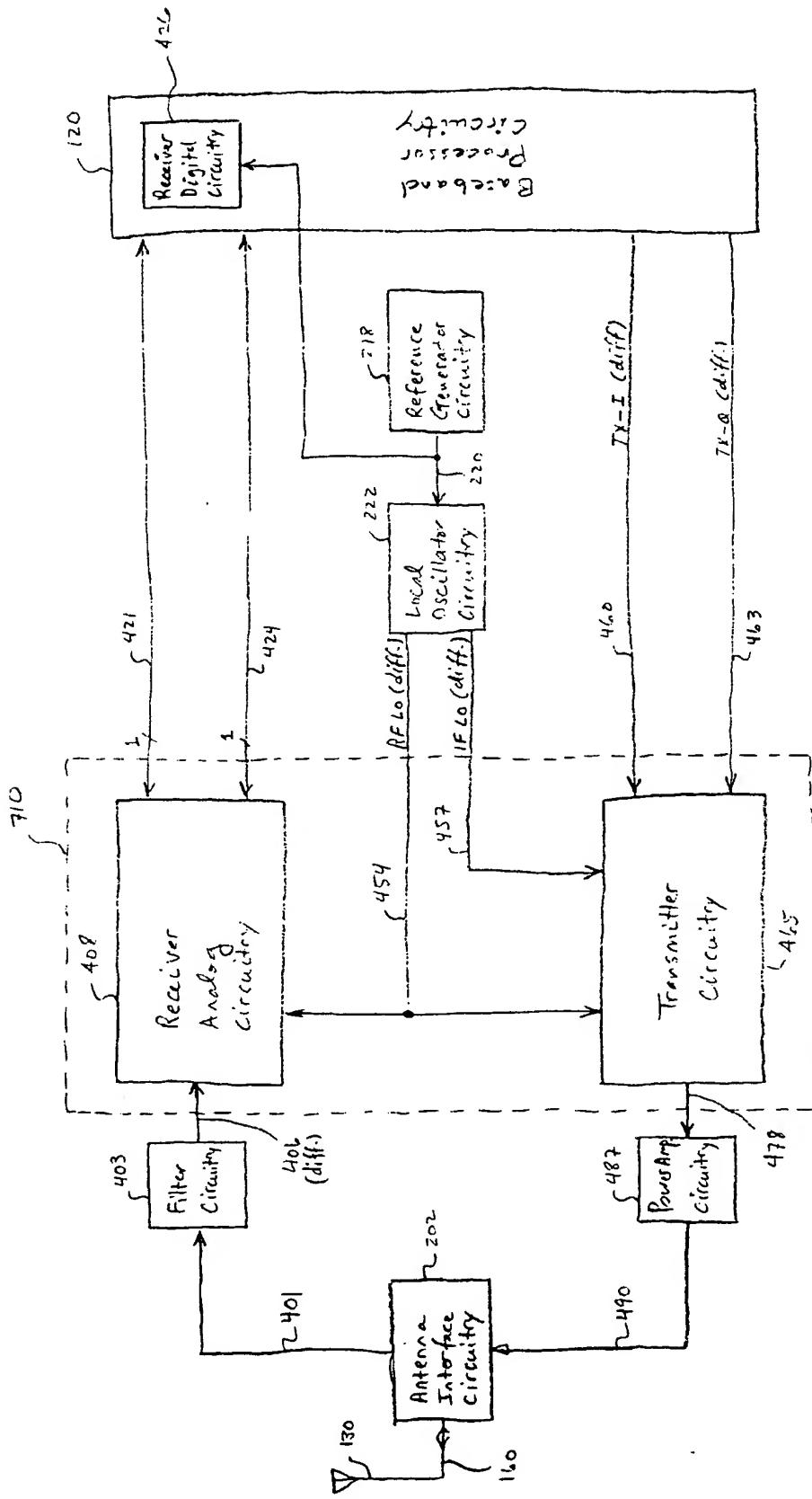


FIG. 7

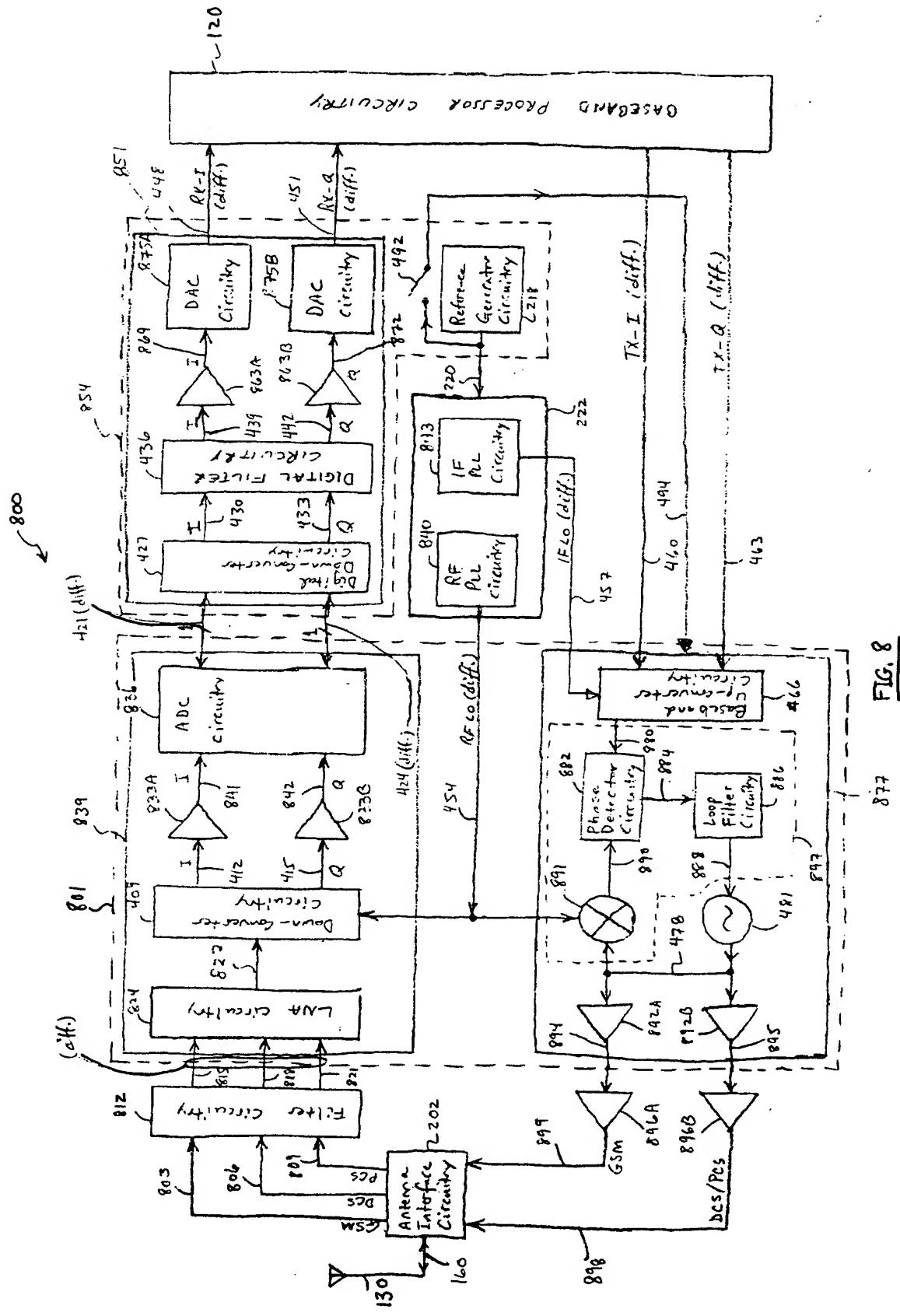


FIG. 8

900A

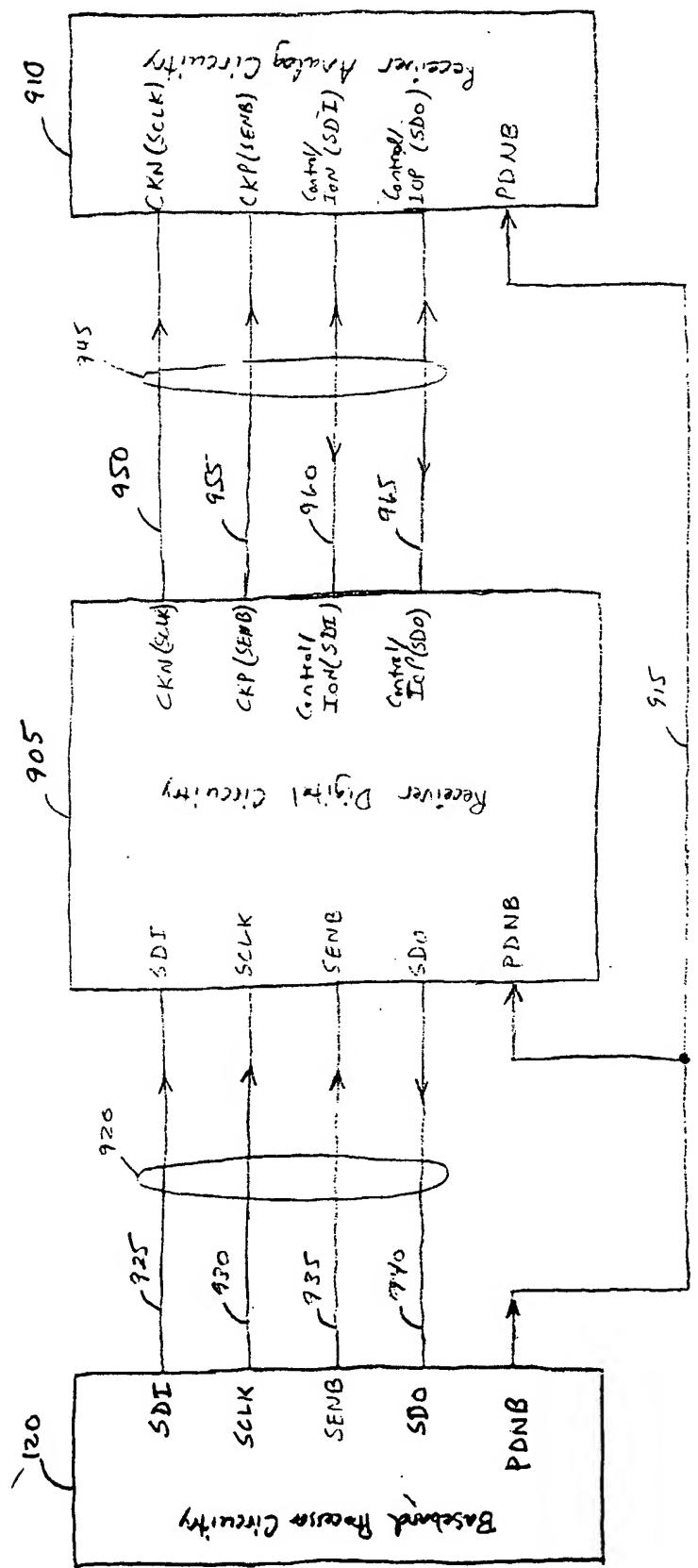


FIG. 9A

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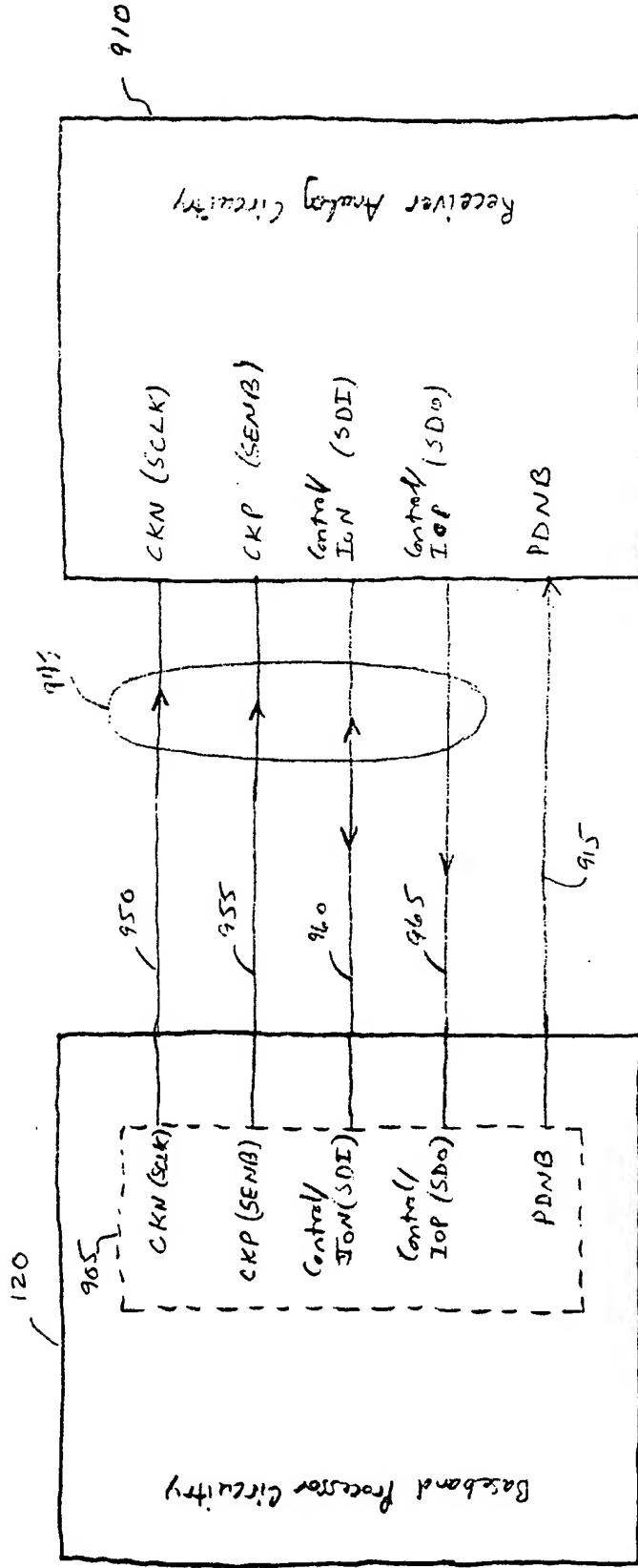


FIG. 9B

1000

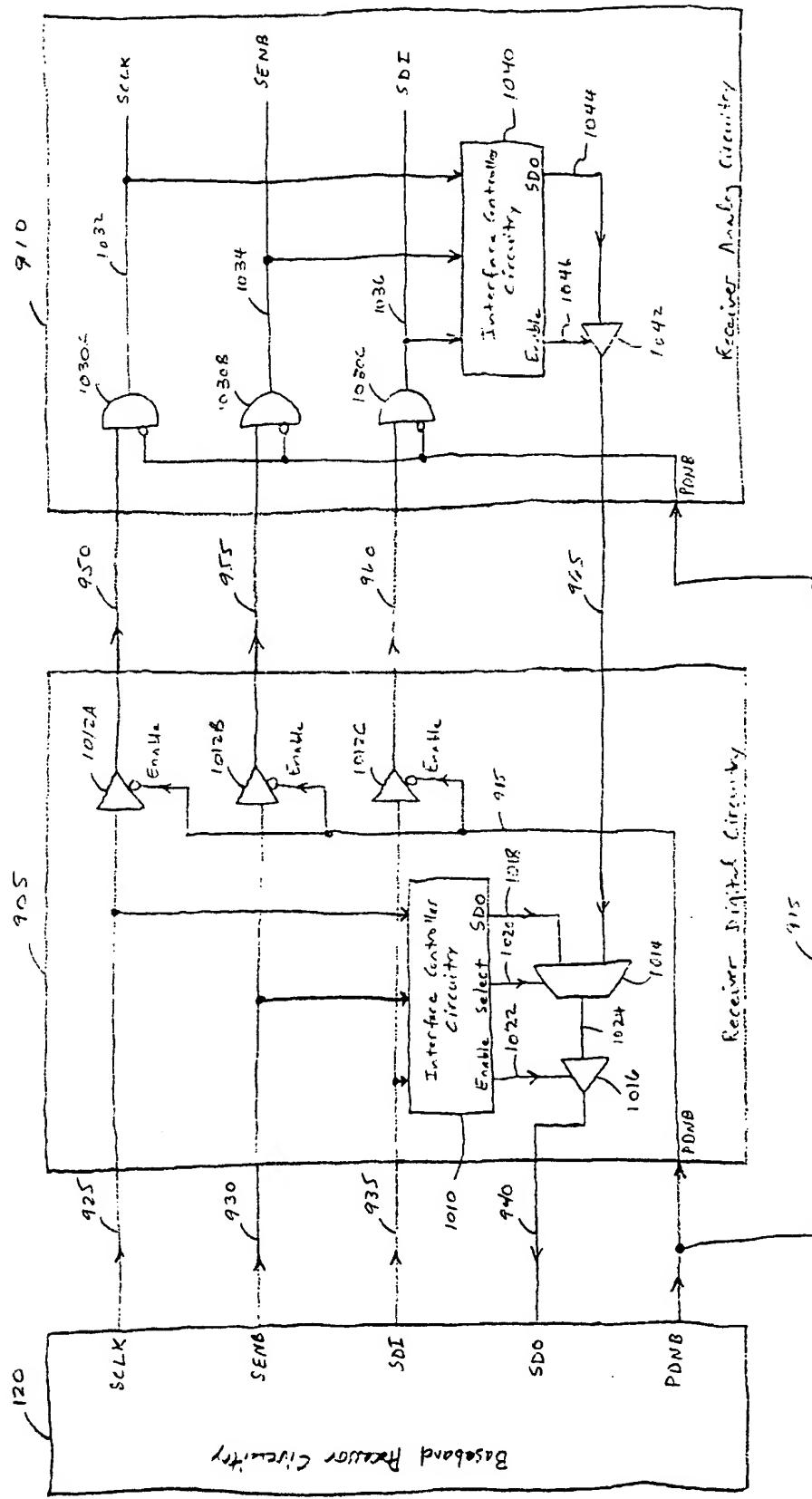
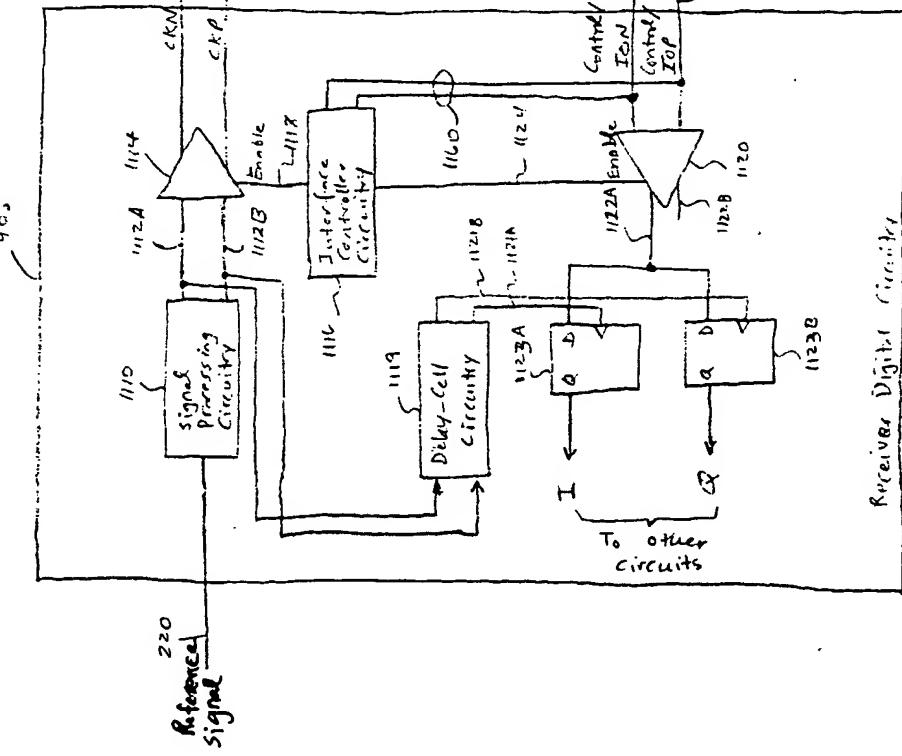
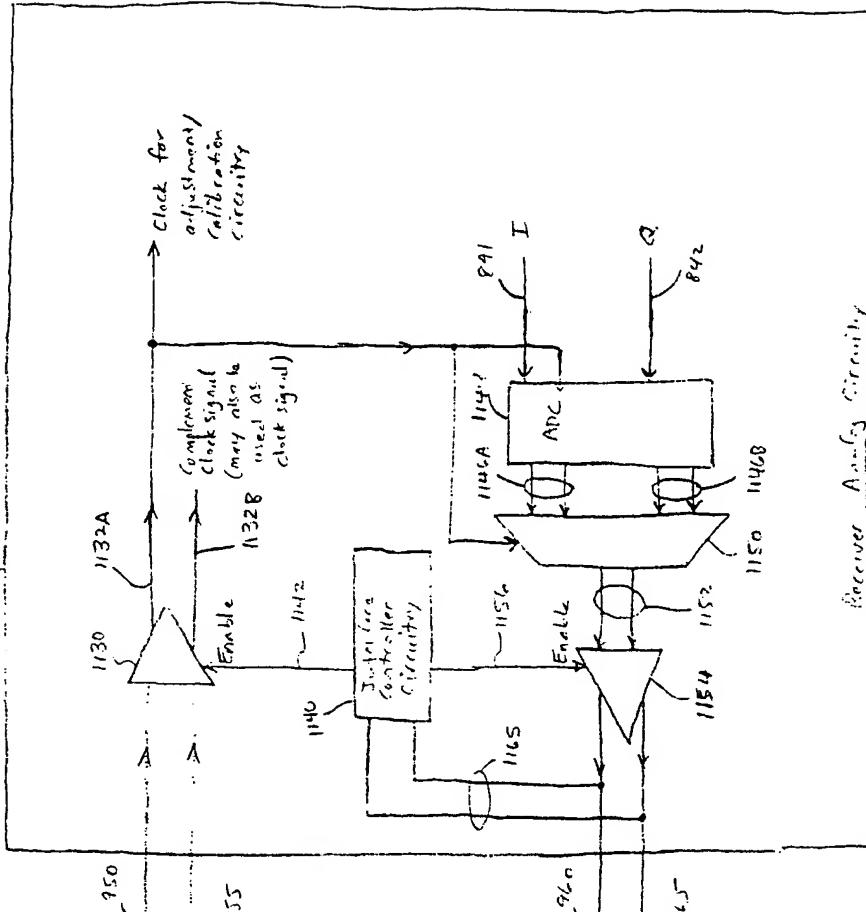


FIG. 10



Receivers Digital Circuits



Preamplifier Analysis Circuit

FIG. 11A

11008

5

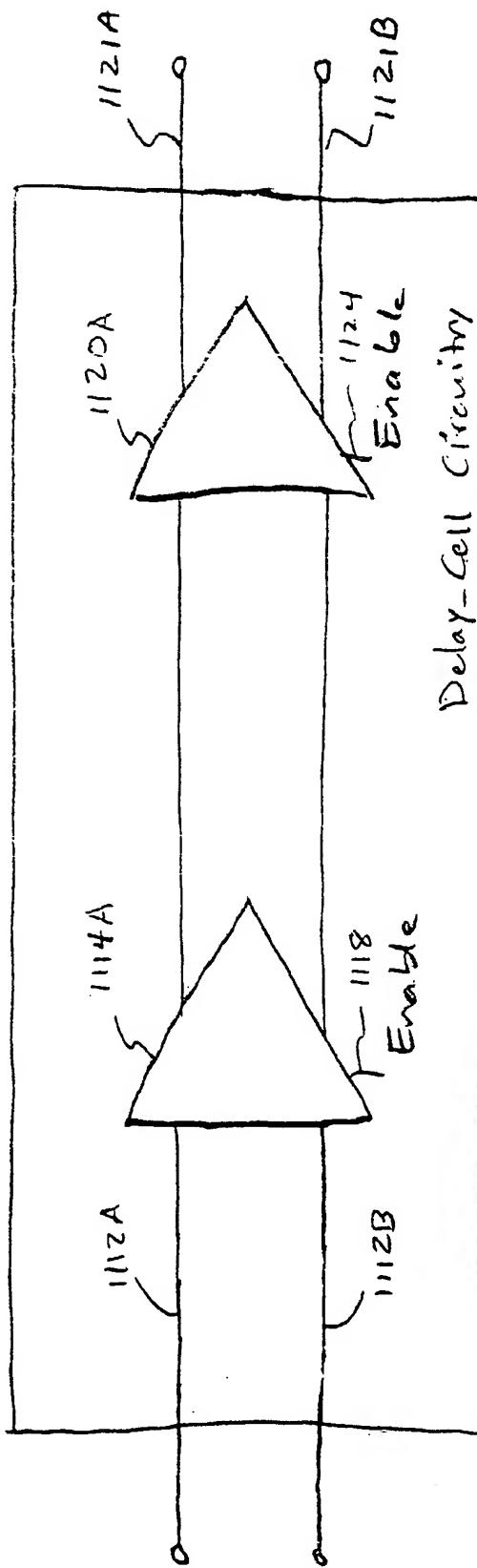


FIG. 11B

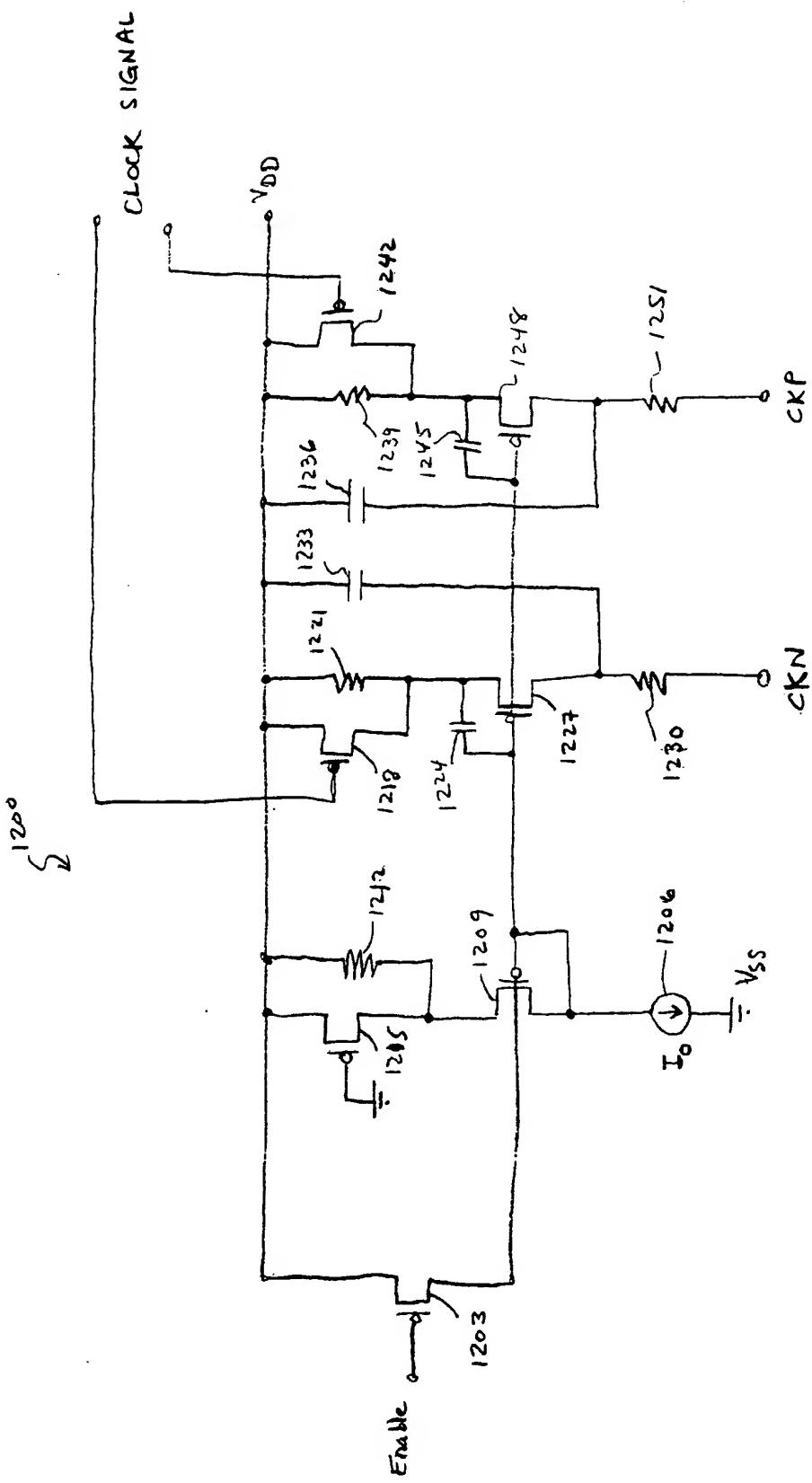
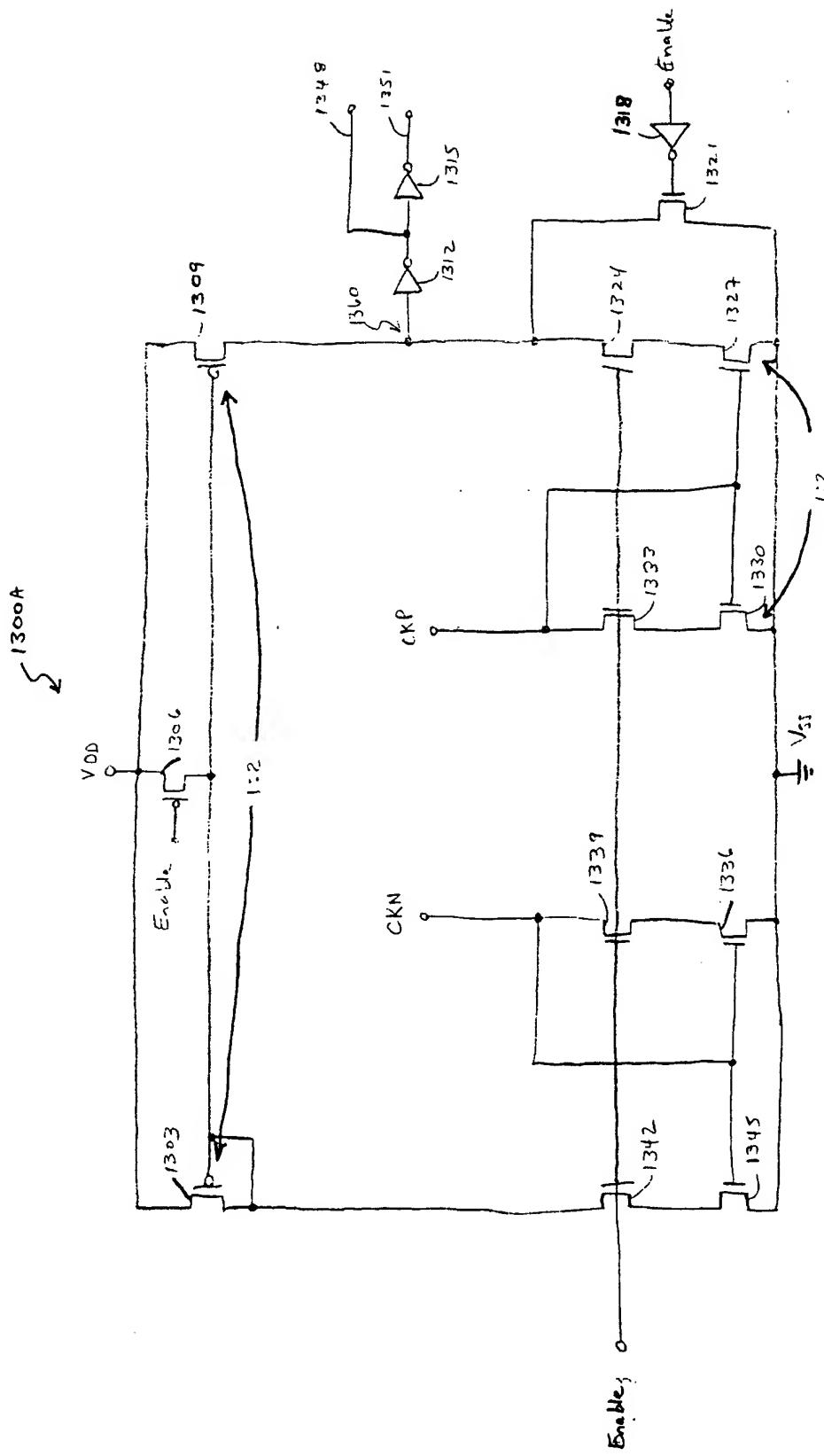


FIG. 12



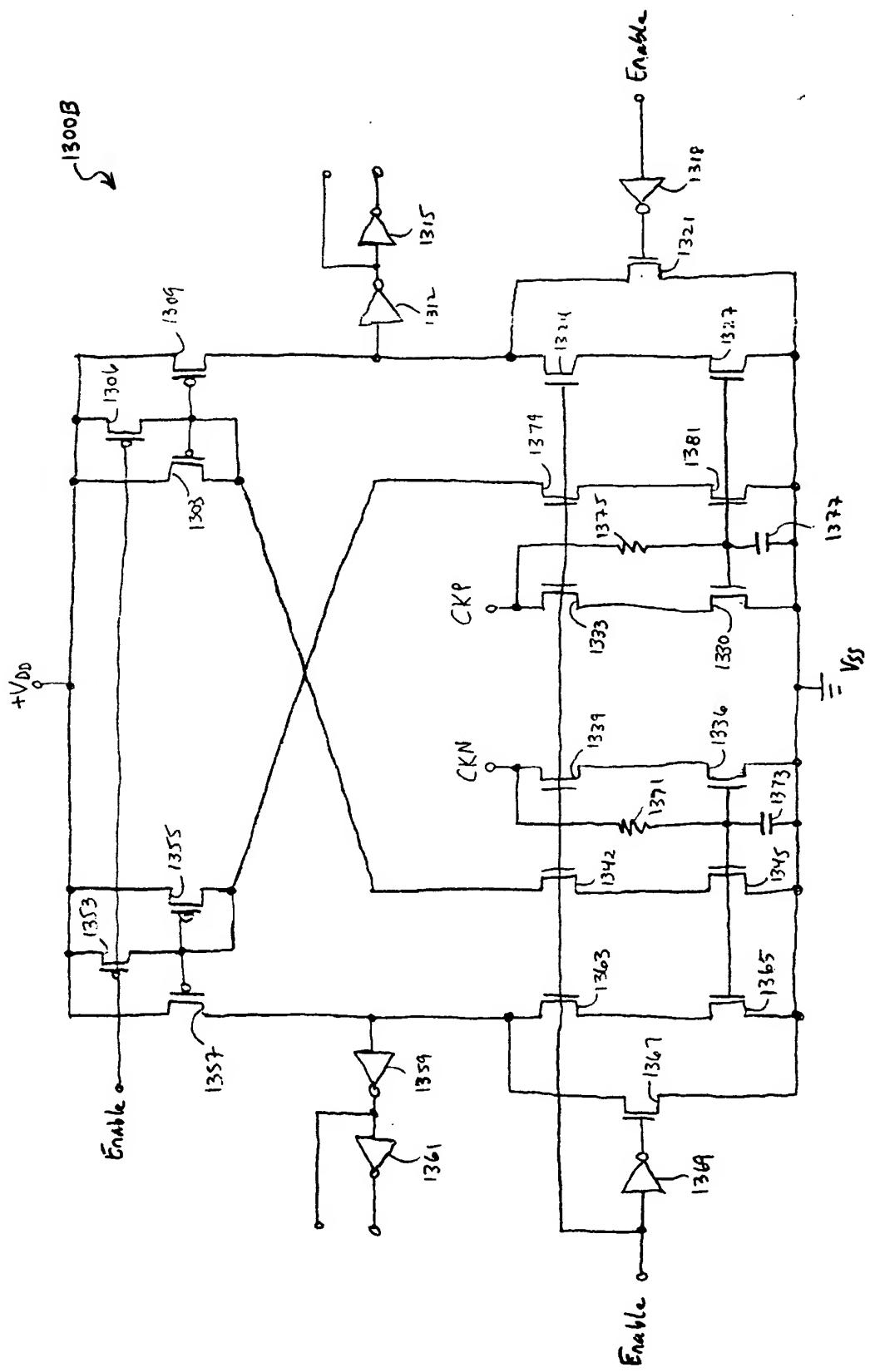


FIG. 13B

\curvearrowleft 1400

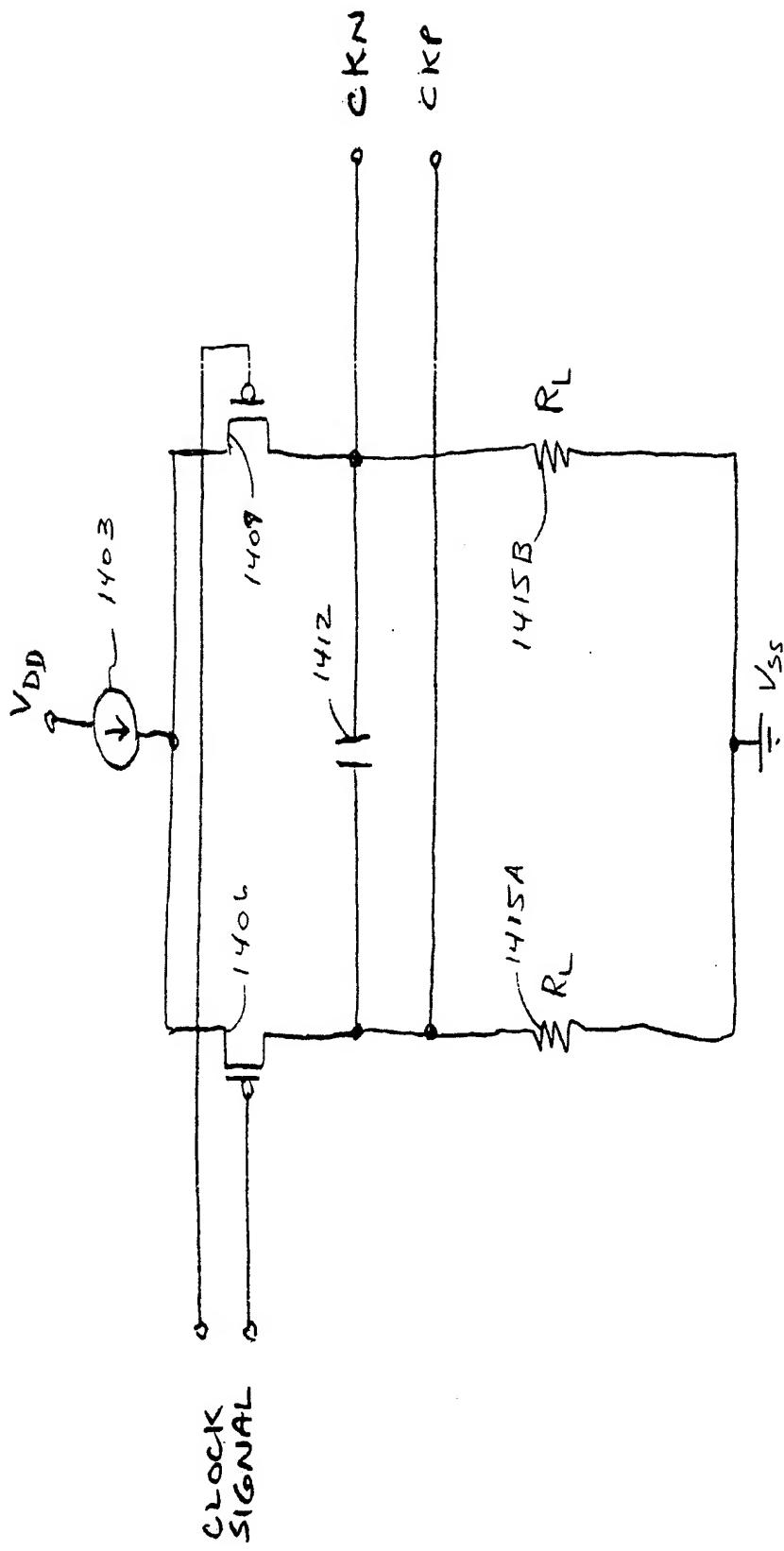


FIG. 14

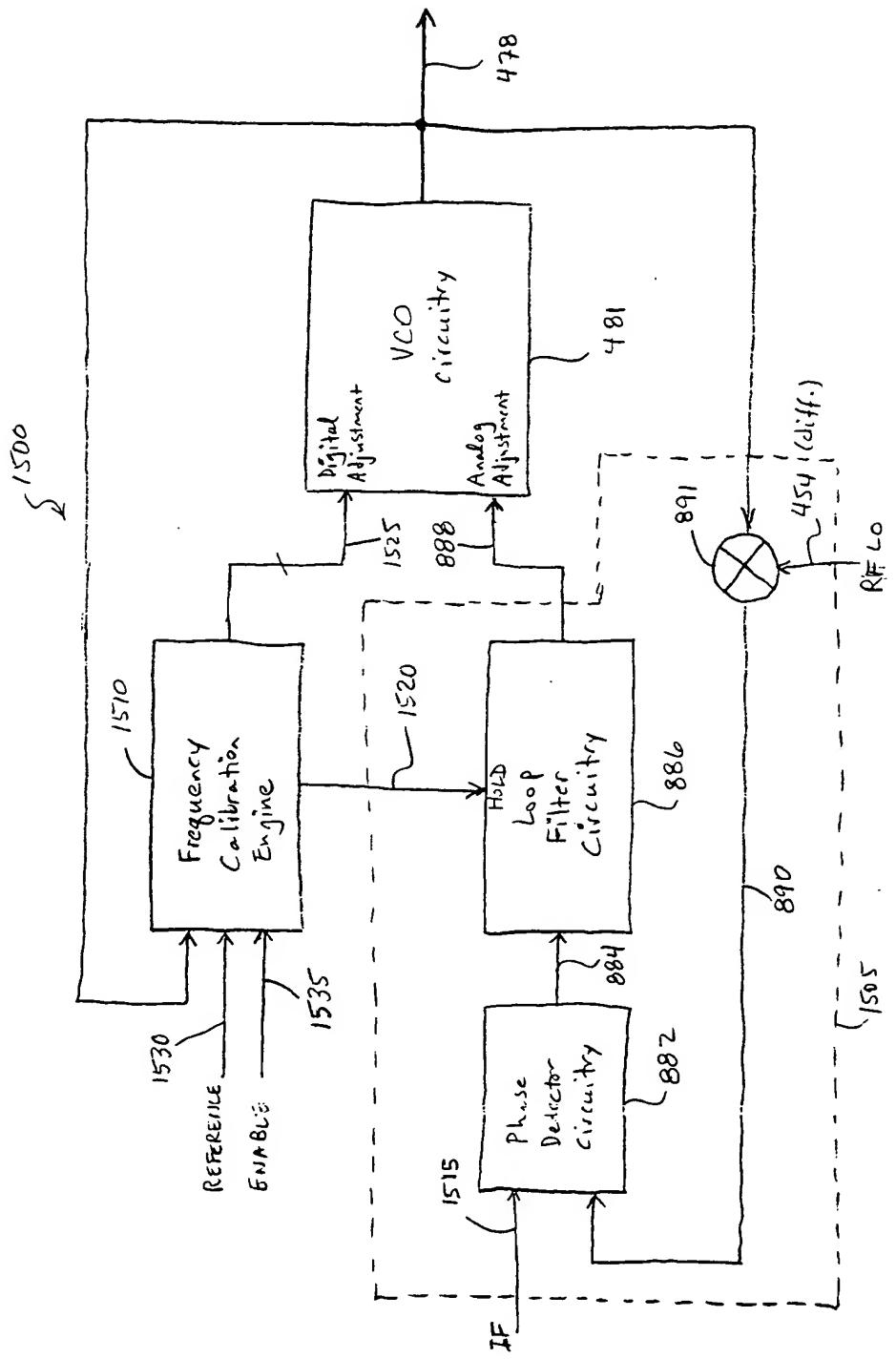


FIG. 15

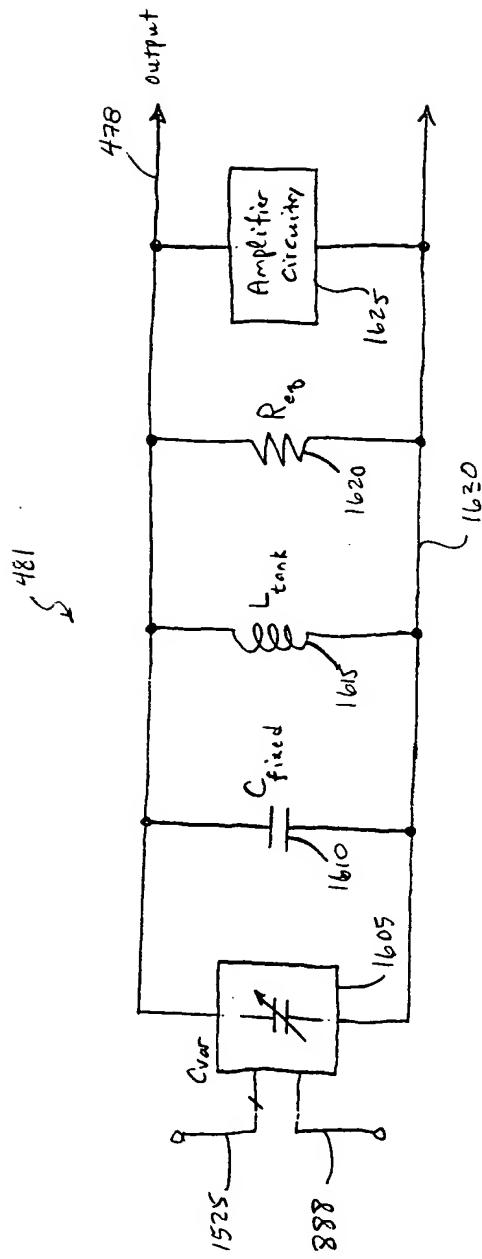


FIG. 16

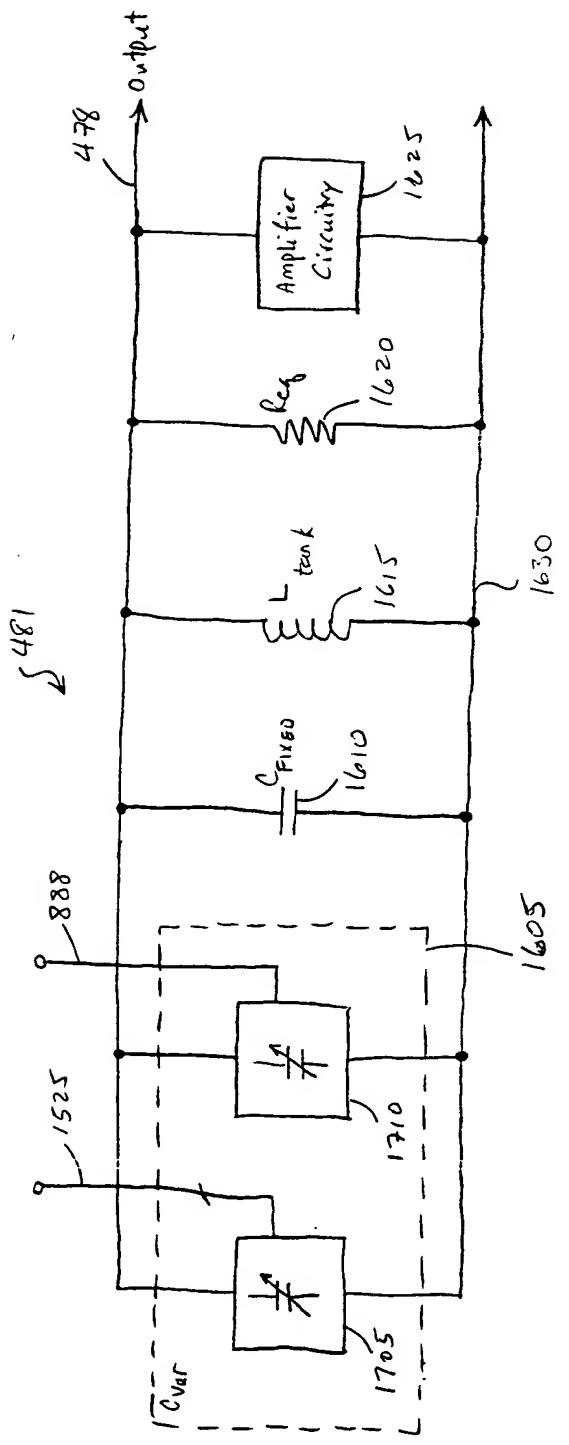


FIG. 17.

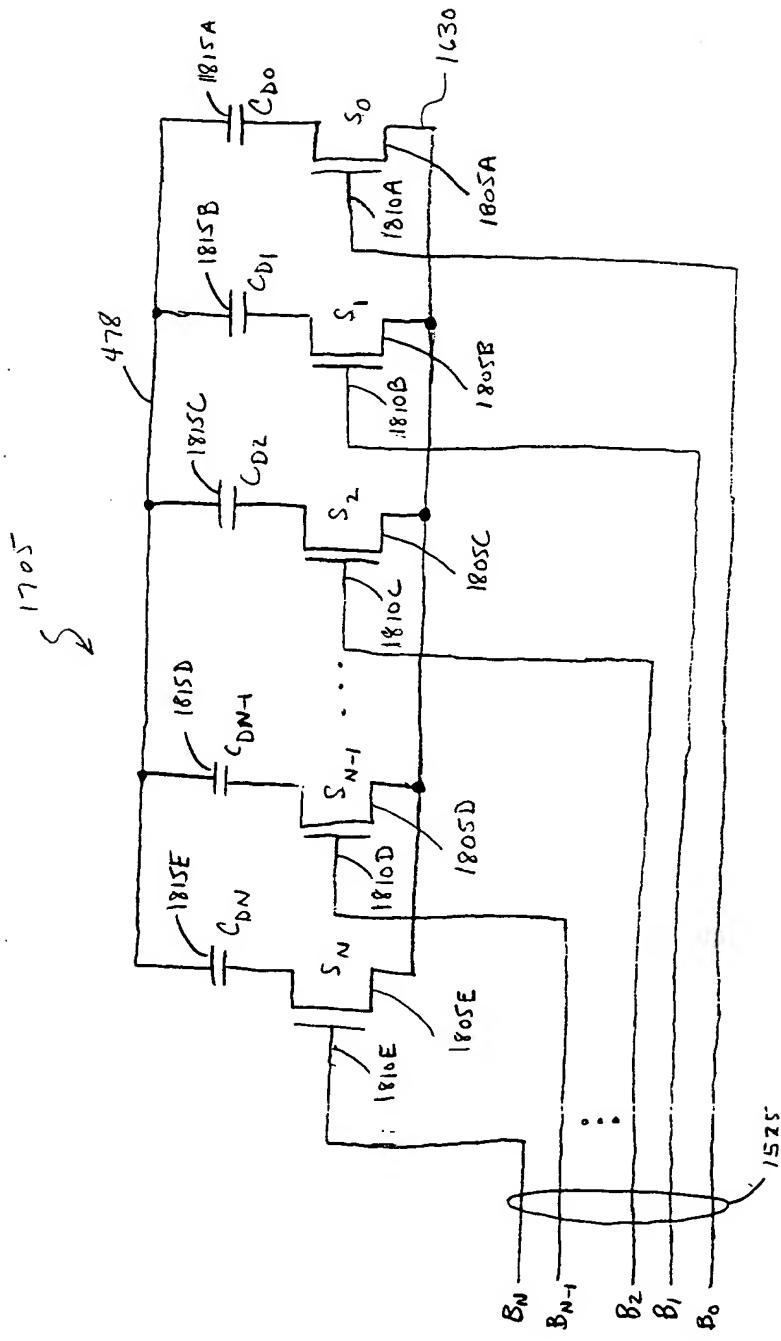


FIG. 18

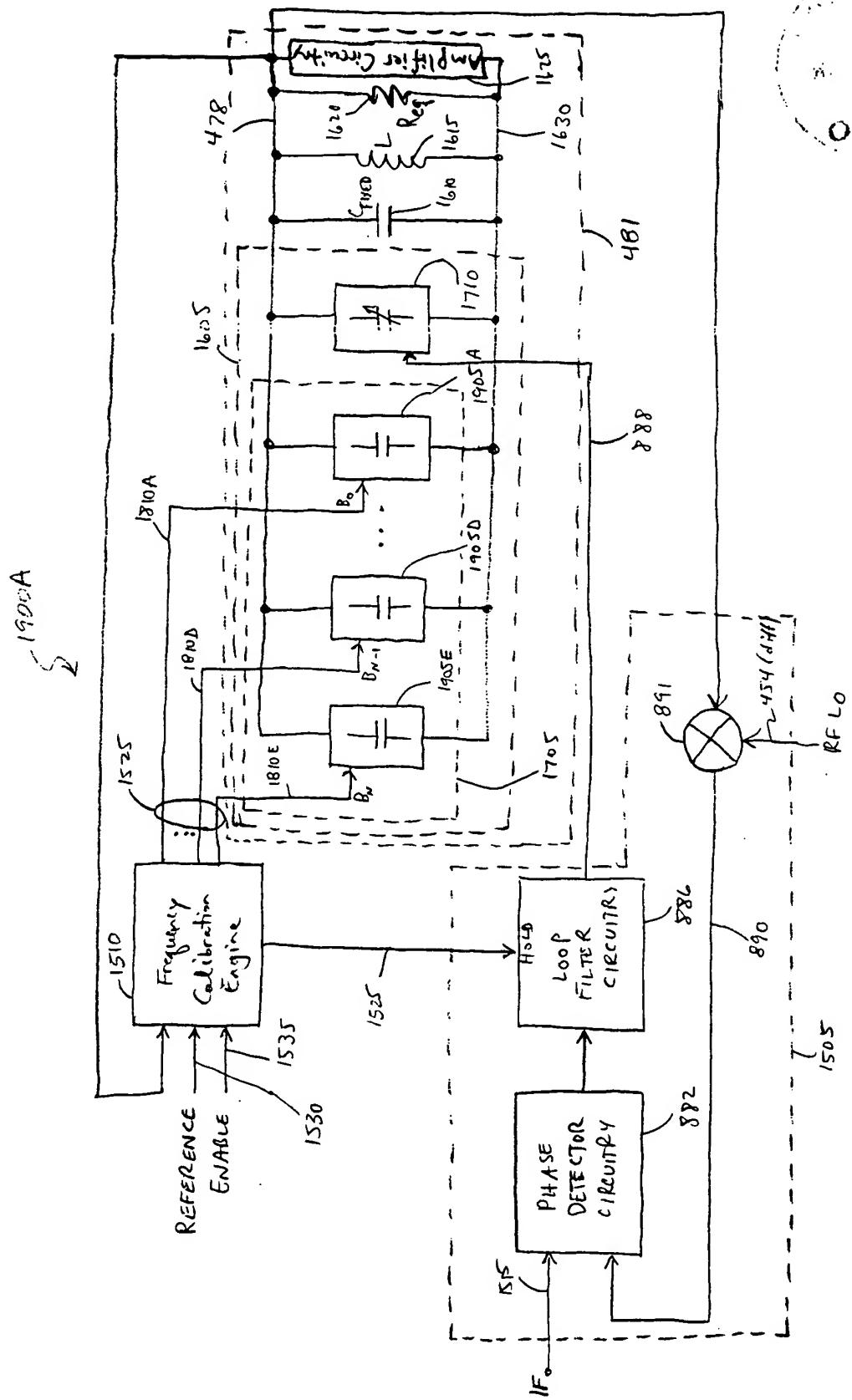


FIG. 19A

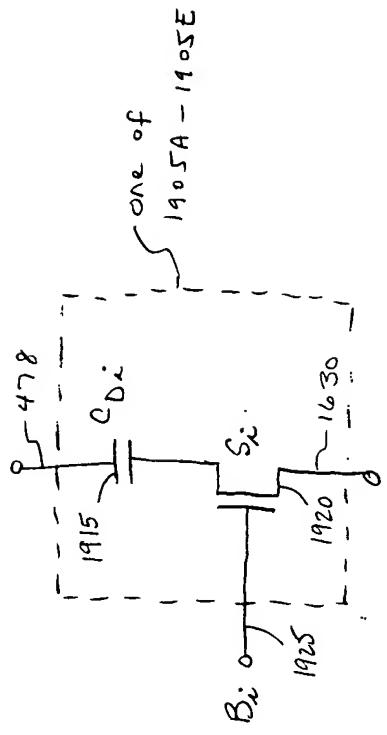


Fig. 19B

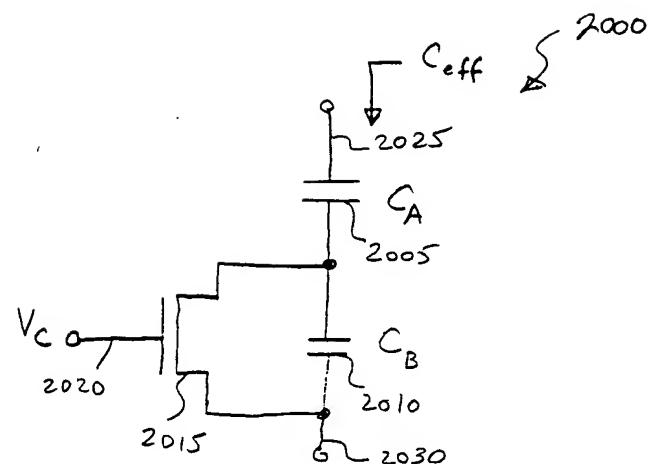


FIG. 20

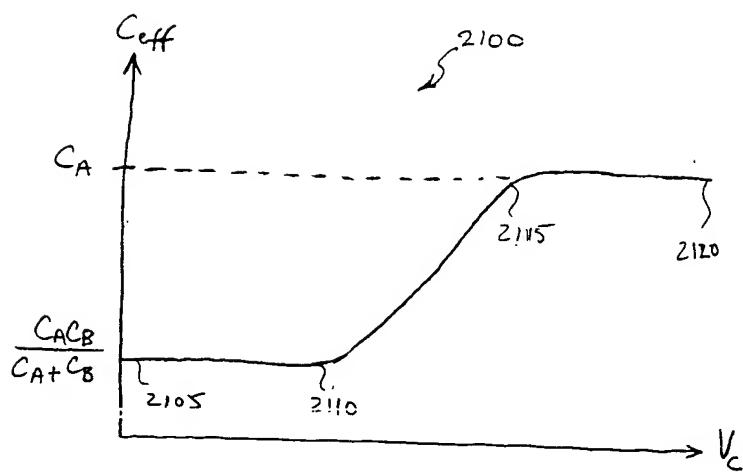


FIG. 21

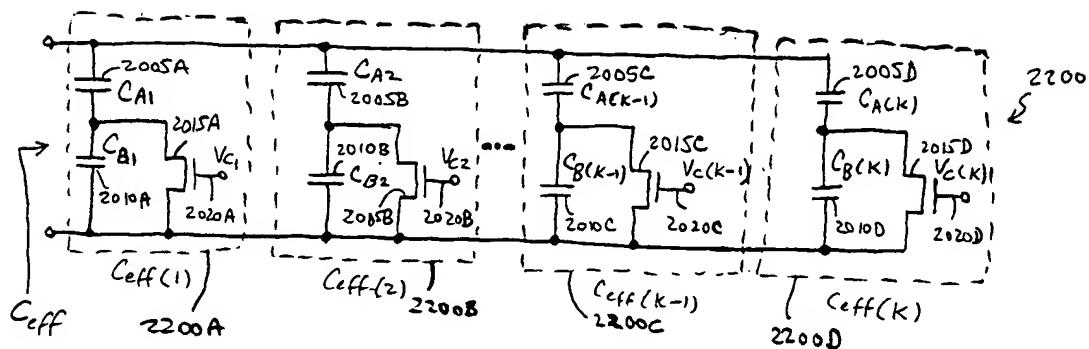


FIG. 22

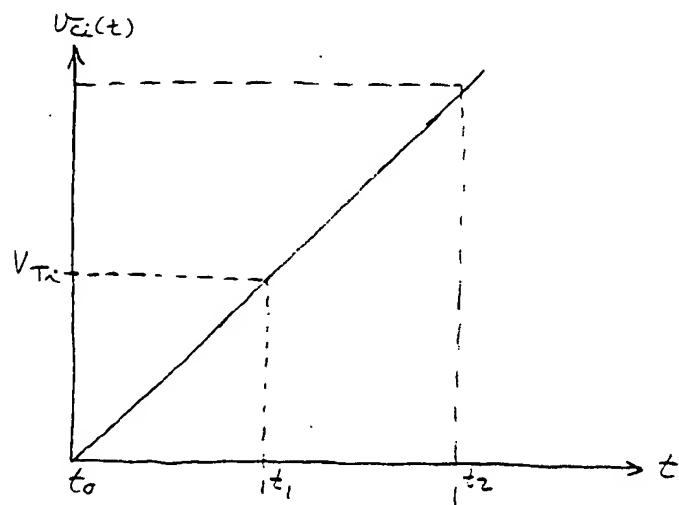


FIG. 23A

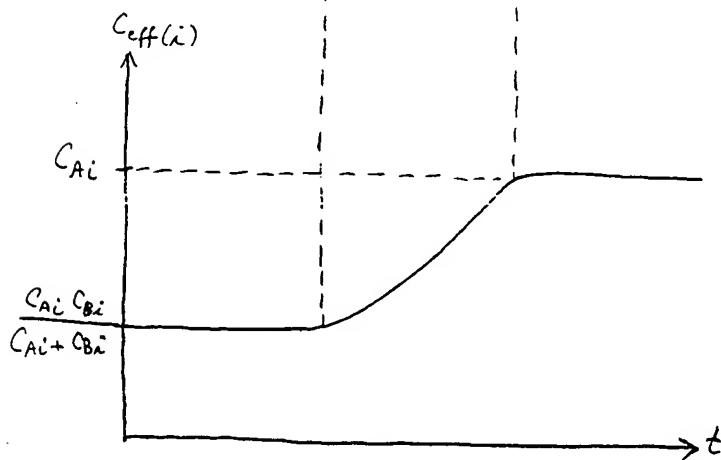


FIG. 23B

FIG. 24A

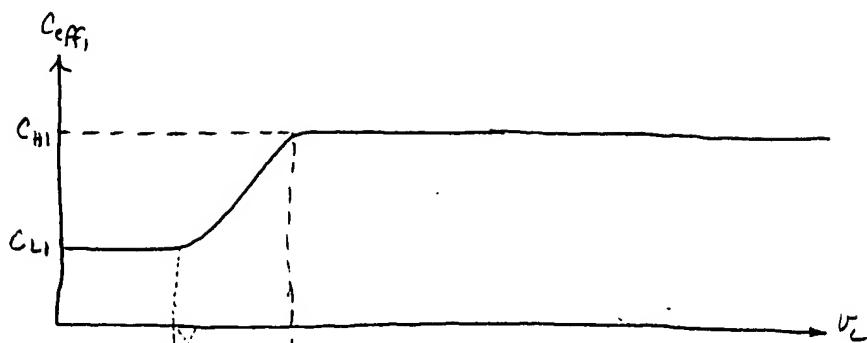


FIG. 24B

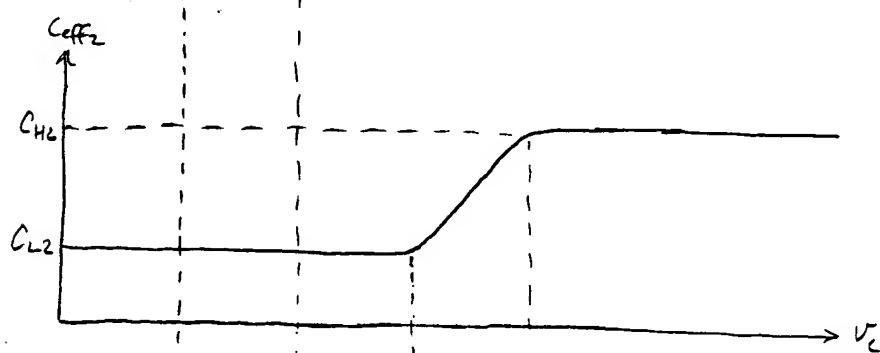


FIG. 24C

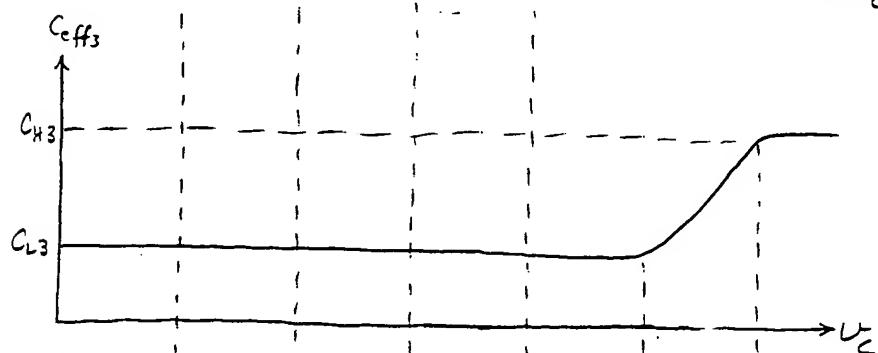
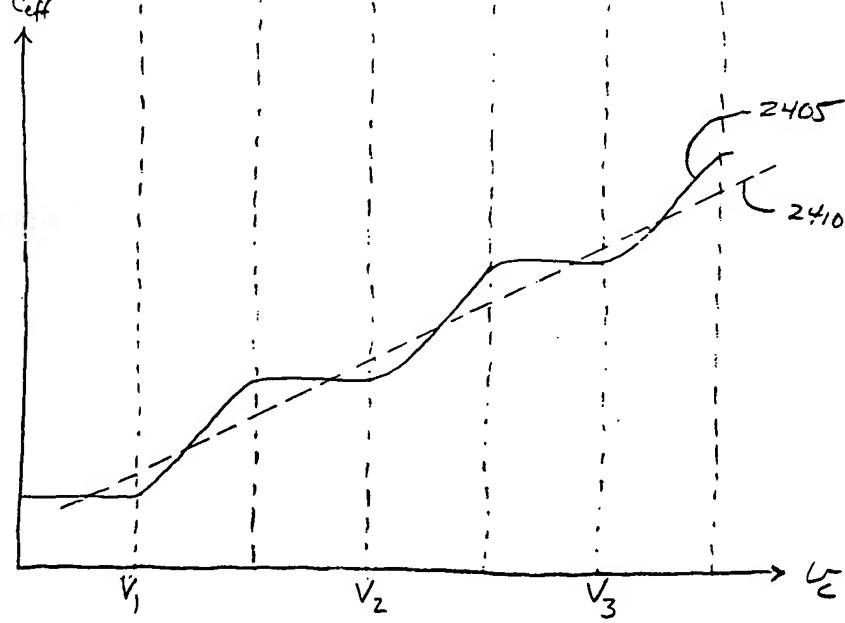


FIG. 24D



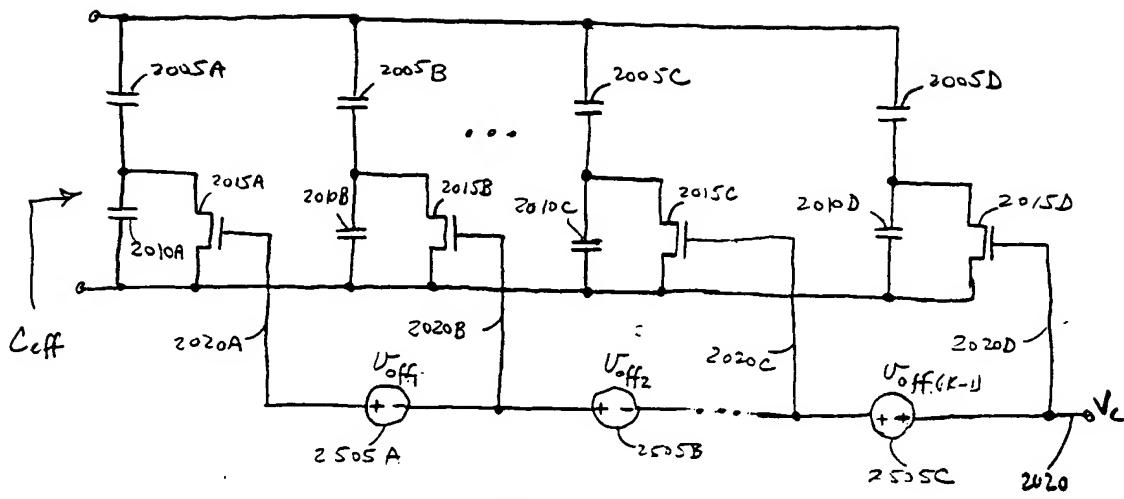


FIG. 25

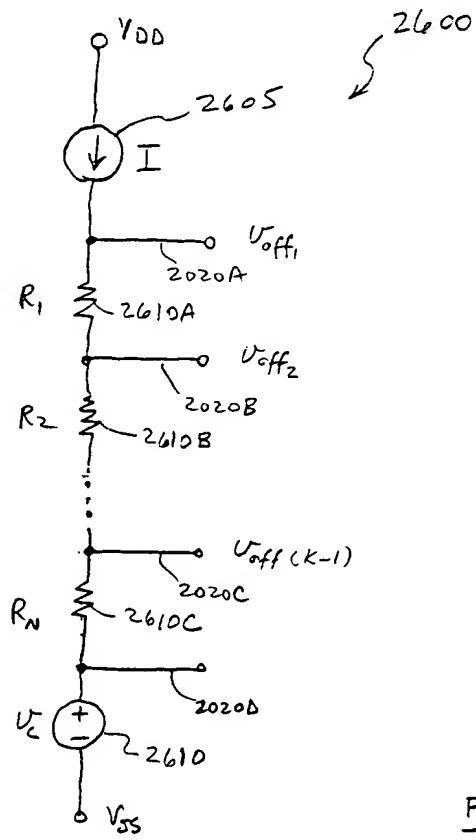


FIG. 26

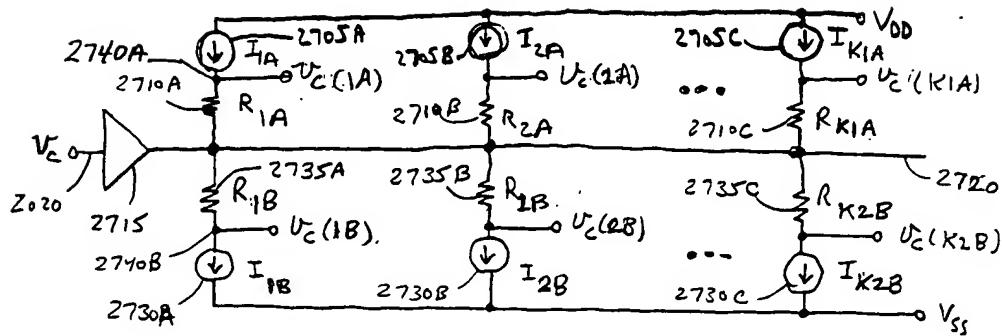


FIG. 27

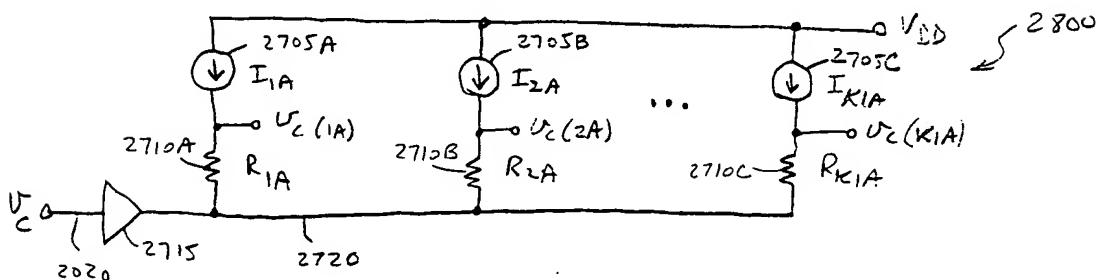


FIG. 28

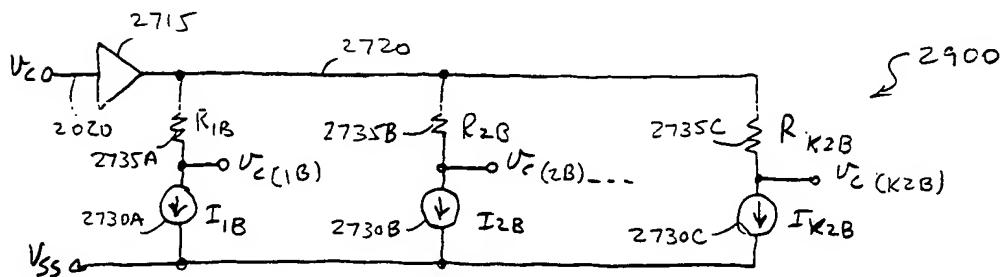


FIG. 29

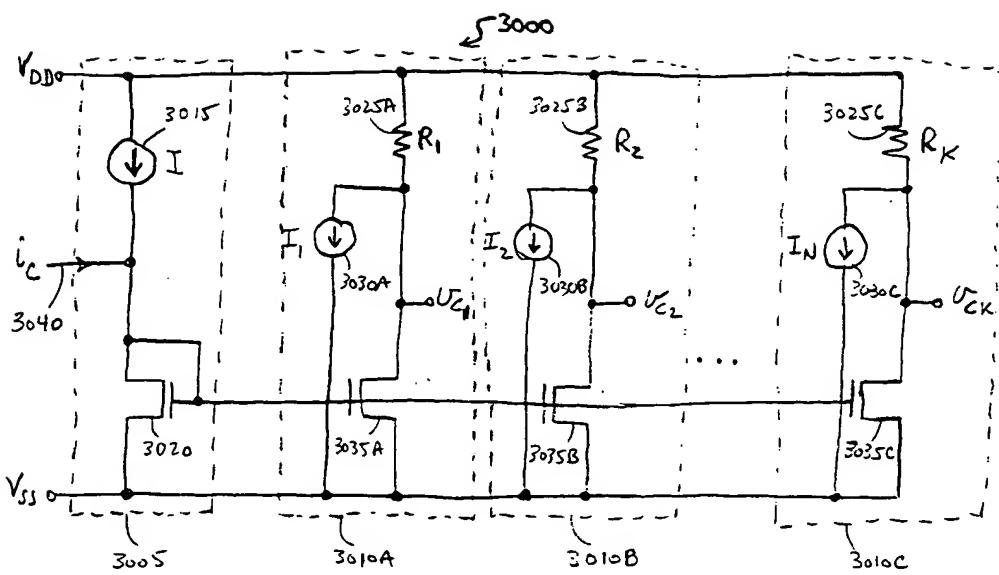


FIG. 30

3100A

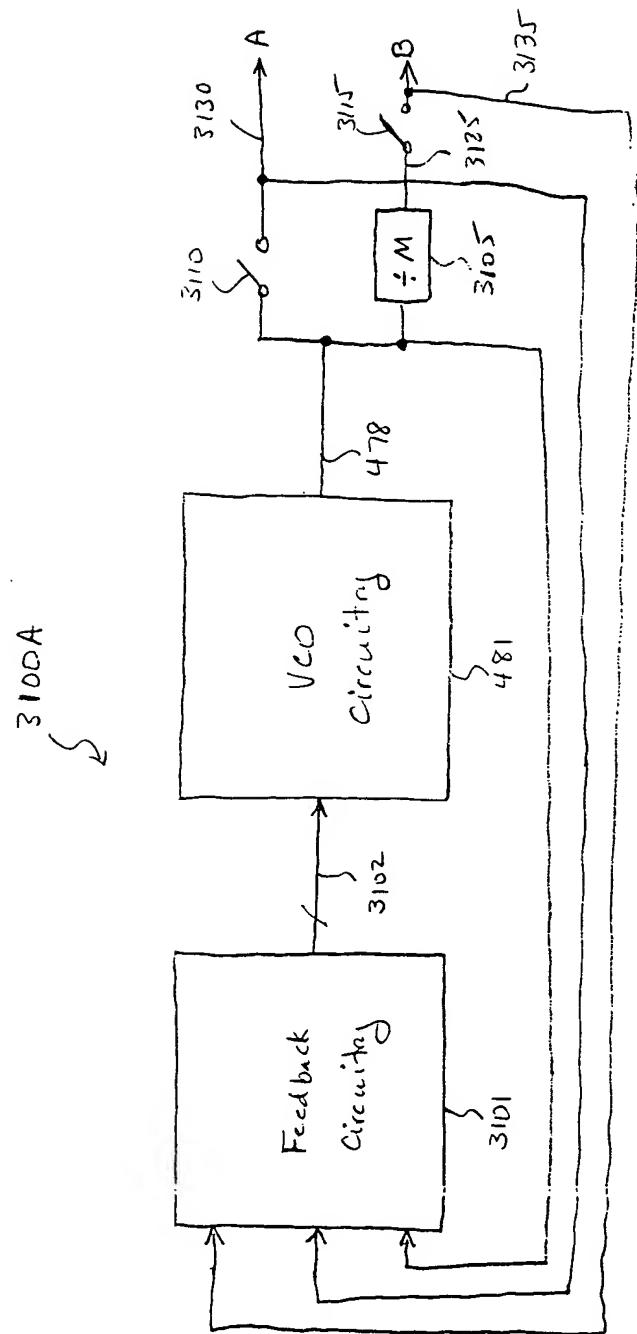


FIG. 31A

3100B

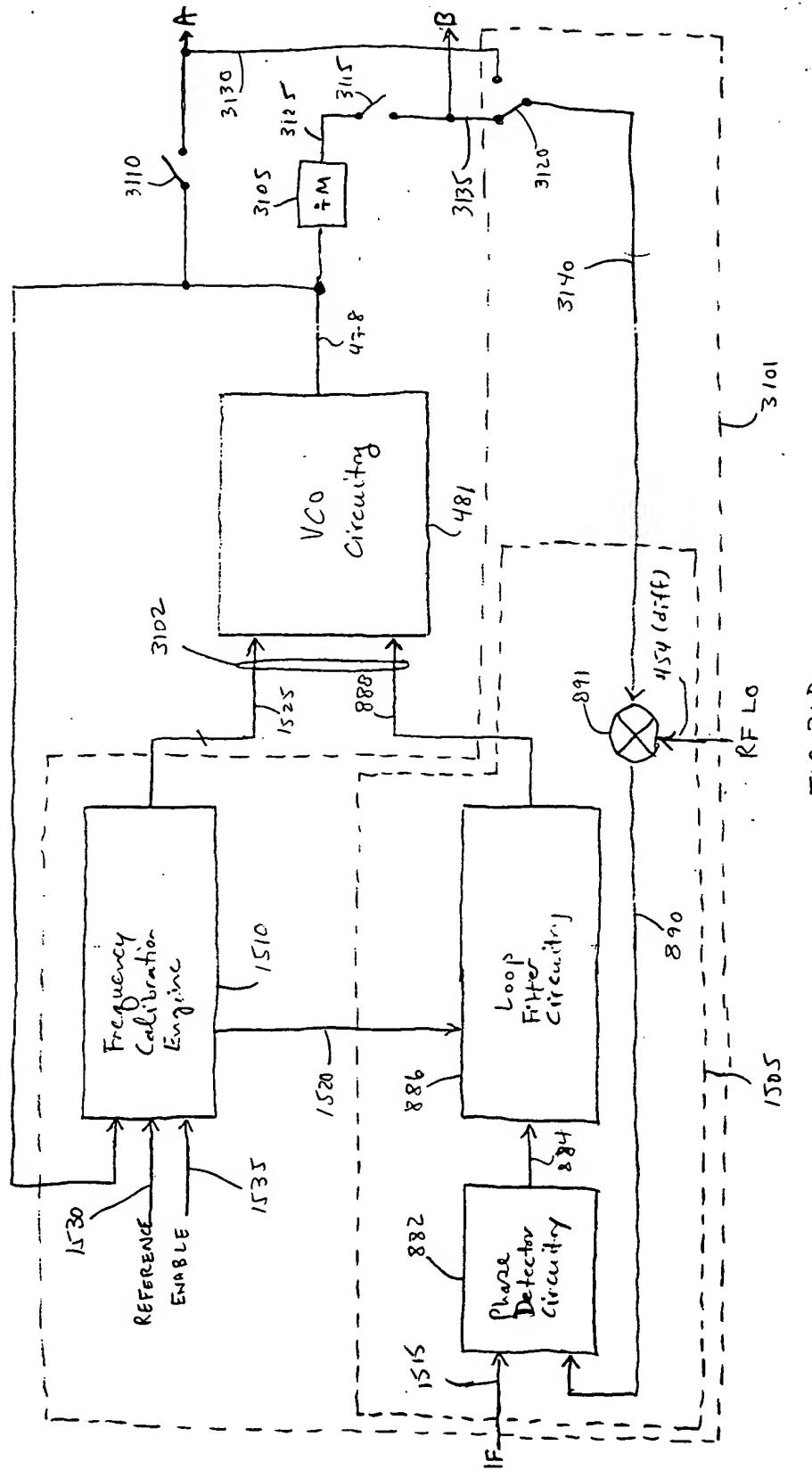
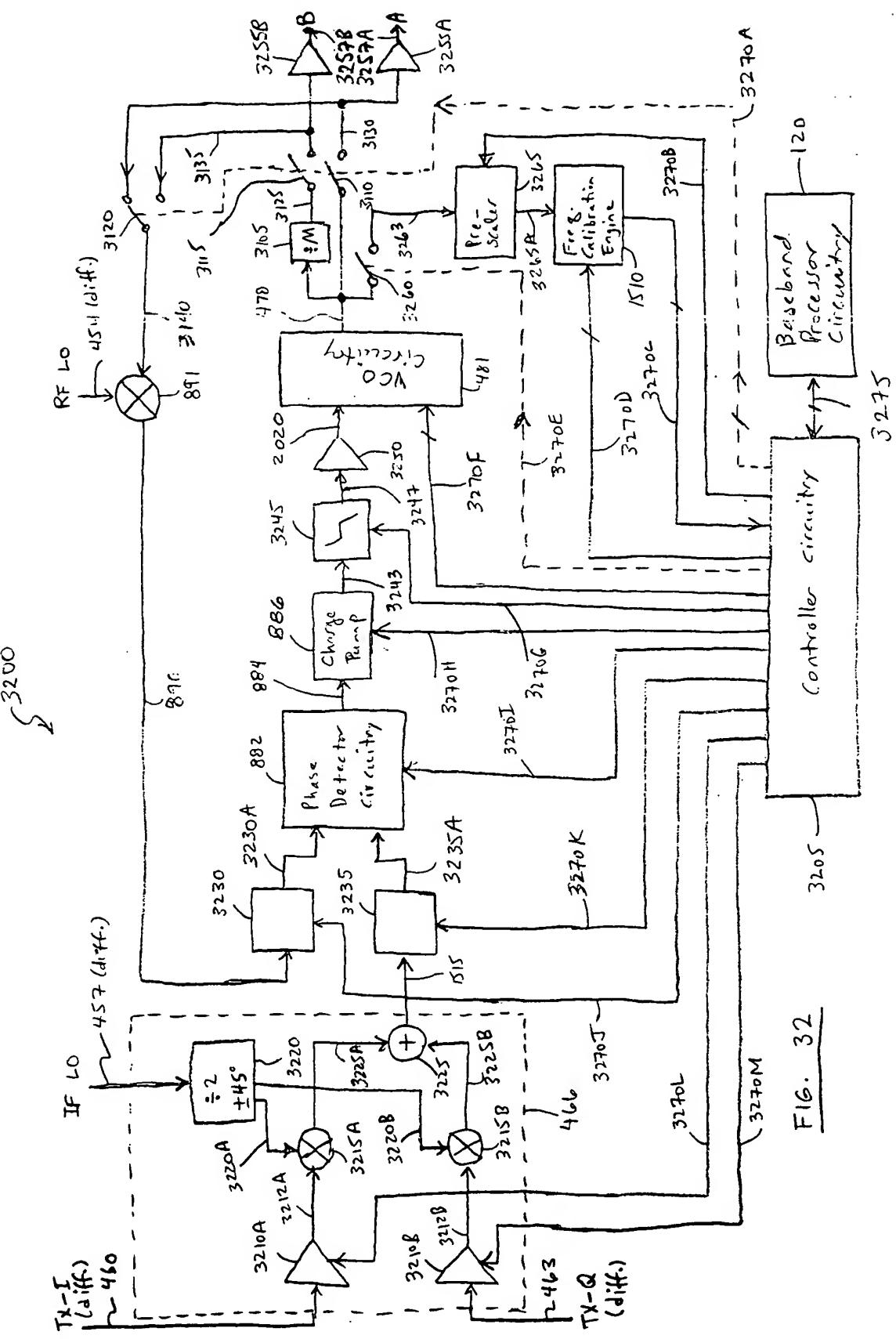


FIG. 31B



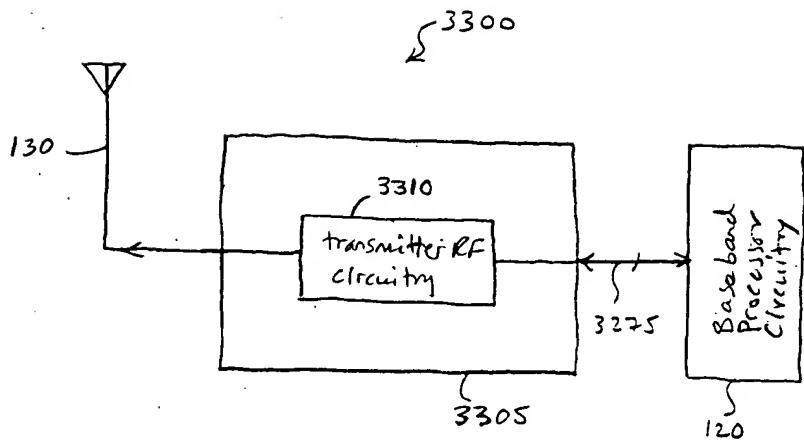


FIG. 33

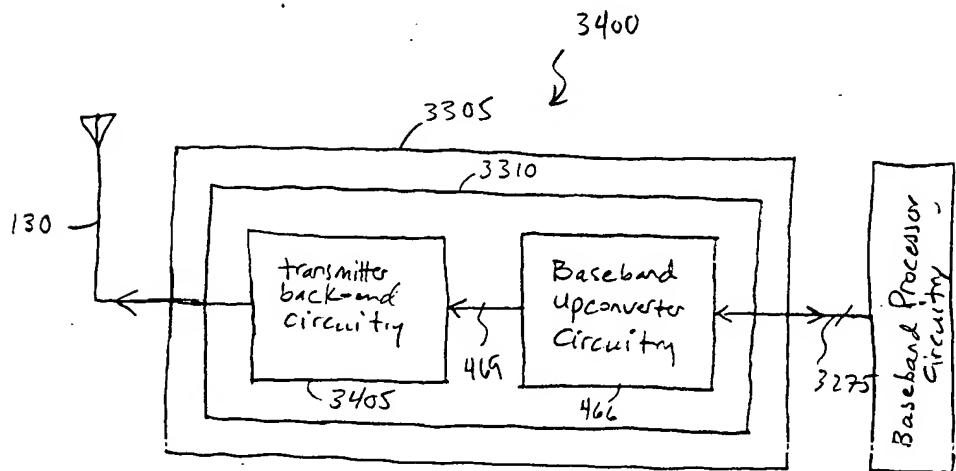


FIG. 34

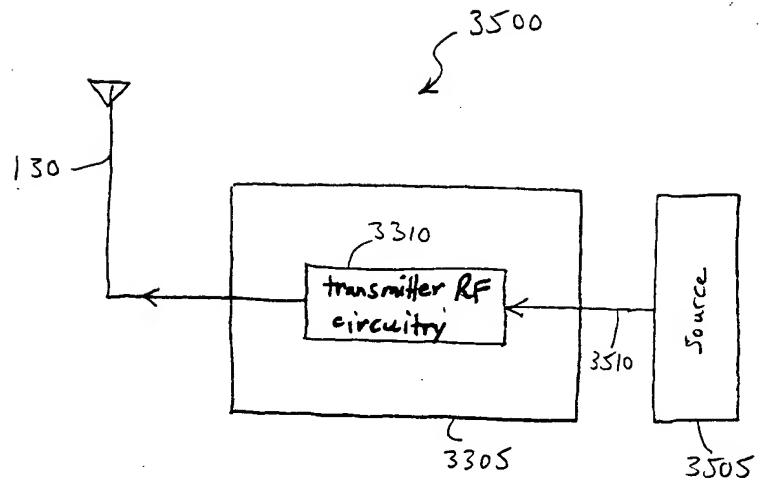


FIG. 35